DAYANANDA SAGAR UNIVERSITY
SCHOOL OF ENGINEERING
Devarakaggalahalli, Harohalli, Kanakapura Road,
Ramanagara Dt., Bengaluru – 562 112
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minutes of 12th BOS

School	:	School of Engineering
College	:	Dayananda Sagar University
Programme/Department	:	Electronics and Communication Engineering
Mode of Meeting	:	Hybrid Mode
Meeting Link	:	https://meet.google.com/dbr-axwv-epp?hs=224
Date and Time	:	23rd May 2025, 10:00 AM
Venue	:	3rd Floor, A308, A Block

AGENDA

The agenda for the BOS meeting is as follows:

The agenda for the BOS meeting is as follows:

I. Curriculum Development and Approval

- B. Tech ECE Programs:
  - Approval of the scheme and syllabus for 1st year of 2025-2029 Batch.
  - Discussion and approval of the scheme and syllabus for 2nd, 3rd, and 4th year of 2024-2028 Batch.
  - Discussion and approval of the scheme and syllabus for 3rd, and 4th year of 2023-2027 Batch.
  - Approval of the scheme and syllabus for 4th year of 2022-2026 Batch.
  - Open Electives: Approval for elective courses offered in the 3rd and 4th years of B. Tech ECE programs.

II. Ratification of the Curriculum of M. Tech Programs of 2025-2027 Batch.

- Discussion on the curriculum of M. Tech in Embedded Systems of 2025-2027 Batch.

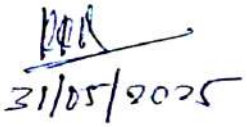
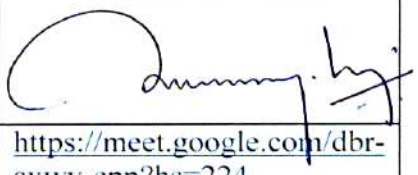
III. Doctoral Programs

- Discussion on the course syllabus offered for the department's PhD program.

IV. Other Academic Matters

- Discussion regarding the strategic goals and quality improvement in the ECE Department. Open forum for any other academic concerns.

Members Present:

Sl. No	Name	Affiliation and Contact Details	Signature
1	Dr. Arun Balodi	Chairman and Professor, Department of ECE, SOE, DSU Email: chairman-ece@dsu.edu.in Mobile: 963405552	
2	Dr. C P Ravikumar	Adjunct Professor, Indian Institute of Information Technology Dharwad, Karnataka. Email: cpravikumar@hotmail.com Mobile: 9845542797	ABSENT
3	Mr. Kiran Kumar Kulkarni	Global Industry Manager-Engineering and Technology Services, MathWorks India Pvt Ltd. Email: kkulkarn@mathworks.com Mobile: 9538060004	
4	Dr. Srinivas Talabattula	Associate Professor - Electrical Communication Engineering Indian Institute of Science, Bangalore Email: tsrinu.iisc@gmail.com tsrinu@ece.iisc.ernet.in Mobile: 9740922377	
5	Dr. Vasantha M H	Associate Professor, Department of ECE, NIT, Goa Email: vasanthmh@nitgoa.ac.in Mobile: 8217046754	
6	Dr. S. Arungalai Vendan	Professor, Department of ECE, SOE, DSU Email: arungalai-ece@dsu.edu.in Mobile: 9994748264	
7	Dr. Vinu.R	Associate Professor, Department of ECE, SOE, DSU Email: vinur-ece@dsu.edu.in Mobile: 9633440988	https://meet.google.com/dbr-axwv-epp?hs=224
8	Dr. Theodore Chandra S	Associate Professor, Department of ECE, SOE, DSU Email: theodore-ece@dsu.edu.in Mobile: 9585595328	https://meet.google.com/dbr-axwv-epp?hs=224
9	Mr. Nadeem Pasha	Assistant Professor, Department of ECE, SOE, DSU Email: nadeempasha-ece@dsu.edu.in Mobile: 7019929615	
10	Ms. Rida Fatima	Supply Chain Engineer, BMW India Pvt Limited. Email: rida.fatima424@gmail.com Mobile: 8105846032	https://meet.google.com/dbr-axwv-epp?hs=224

MINUTES OF MEETING

The Board of Studies meeting of the Department of Electronics and Communication Engineering was convened on 31st May 2025 at 10:00 AM in Room A308, 3rd Floor, A Block, School of Engineering, Dayananda Sagar University, Harohalli Campus, Bengaluru. The meeting was conducted in hybrid mode to facilitate the participation of both in-person and remote members. The objective of the meeting was to deliberate upon curriculum revisions, academic initiatives, research progress, and departmental developments aligned with emerging technologies and industry demands.

Dr. Arun Balodi, Professor and Chairman of the Department, welcomed all the BoS members and initiated the session with a presentation on the department's vision and mission. He emphasized the department's commitment to academic excellence, innovation in research, and strengthening industry relevance in both undergraduate and postgraduate programs.

Dr. Balodi provided a comprehensive overview of the department's structure, faculty composition, and academic framework. He elaborated on the current curriculum design, highlighted ongoing research efforts, and analyzed student academic performance. Initiatives taken to enhance alumni engagement were also presented, along with details of the professional society chapters established under the IEEE Student Chapter.

The Chairman presented various academic developments including the introduction of new courses, promotion of MOOC-based learning through platforms like NPTEL, and the development of advanced laboratories such as the Cadence Lab, MATLAB Lab, and Embedded Systems Lab. These facilities are aimed at providing students with enhanced hands-on experience.

The department's research accomplishments were highlighted, with publications in reputed journals and presentations at international conferences. Dr. Balodi also informed the members that the department had secured research project grants amounting to ₹43.4 lakhs and had submitted additional proposals worth ₹192 lakhs, signifying the department's growing engagement in externally funded research and innovation.

Dr. Balodi briefed the members about the Industry Conclave on Curriculum Development held on 23rd May 2025. During this conclave, detailed discussions took place between the department and eminent industry experts, including Dr. C. P. Ravikumar (Director, Technical Talent Development, Texas Instrument, Bengaluru) and Mr. Shailesh Sakri (Sr. Director Harman International, Bengaluru), along with senior faculty members. Based on their valuable feedback, revisions were incorporated into the new scheme and syllabus to improve curriculum alignment with industry needs.

In response to a query raised by Mr. Kiran Kumar Kulkarni regarding the implementation of the suggestion from the 11th BoS meeting on introducing a Skill Enhancement Course titled *"Introduction to Model-Based Design"*, Dr. Balodi confirmed that the course has been effectively implemented. He shared that students are actively participating in hands-on simulations and project work, demonstrating a strong understanding of model-based design methodologies.

Further, Mr. Kulkarni suggested integrating AI and ML courses using a methodological toolbox approach, which would empower students with practical tools and frameworks to implement AI/ML solutions effectively.

Dr. Vasantha M. H. recommended that the department focus on securing government-funded research projects in collaboration with VLSI industries. He emphasized the importance of such partnerships in

fostering cutting-edge research, enabling strong industry-academia synergy, and enhancing student and faculty involvement in semiconductor technologies.

Additionally, Dr. Vasantha M. H. encouraged the introduction of practical, hands-on project components within the curriculum, such as soldering, PCB design, circuit simulation, and prototyping. He proposed that by the end of such courses, students should be able to build functional mini-projects like chargers, LED dimmer circuits, temperature sensors, or audio amplifiers, thereby reinforcing practical skills and applied knowledge. Dr. Balodi informed the member that PCB design course is included as a skill enhancement course.

Dr. Arungali Vendan informed the members about the introduction of a new core course titled “Power Electronics and EV,” aligning with the department’s commitment to sustainability.

Dr. Vasantha M. H. advised to facilitate increased engagement with industry-oriented projects. He also recommended that all courses, including professional electives, should be sequenced to ensure the prerequisite knowledge is adequately covered in earlier semesters, thus supporting a logical and progressive academic structure.

Prof. Nadeem Pasha of the department documented the proceedings and read out the minutes of the meeting to all members for confirmation and validation.

Dr. Arun Balodi, Chairman of the Department, concluded the meeting by proposing a vote of thanks. He expressed sincere gratitude to all the external and internal BoS members for their valuable suggestions, insights, and continued support towards the academic and strategic development of the department.

Meeting Adjourned: 1:30 PM

Prepared by:
Prof. Nadeem Pasha
Assistant Professor,
Member- BOS Coordinator
Department of Electronics and Communication Engineering
Dayananda Sagar University

Approved by:
Dr. Arun Balodi
Professor and Chairman
Chairman, Board of Studies
Department of Electronics and Communication Engineering

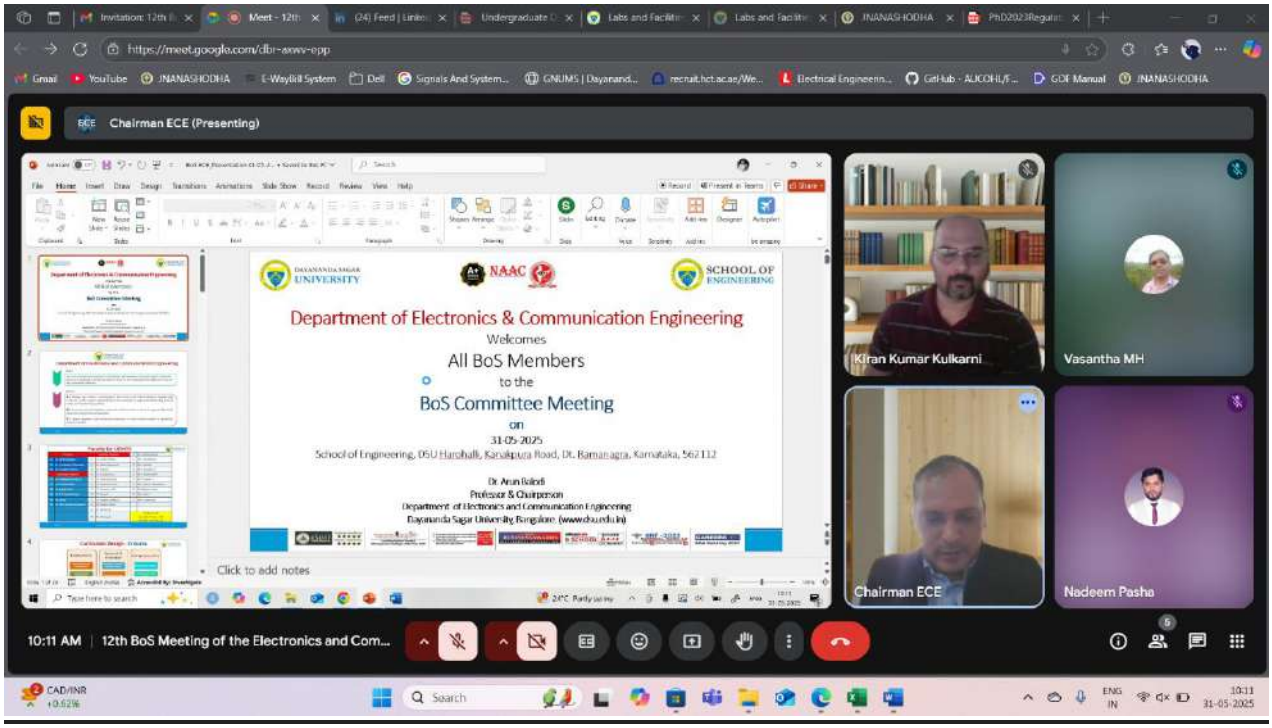
12th BoS Action Taken Report

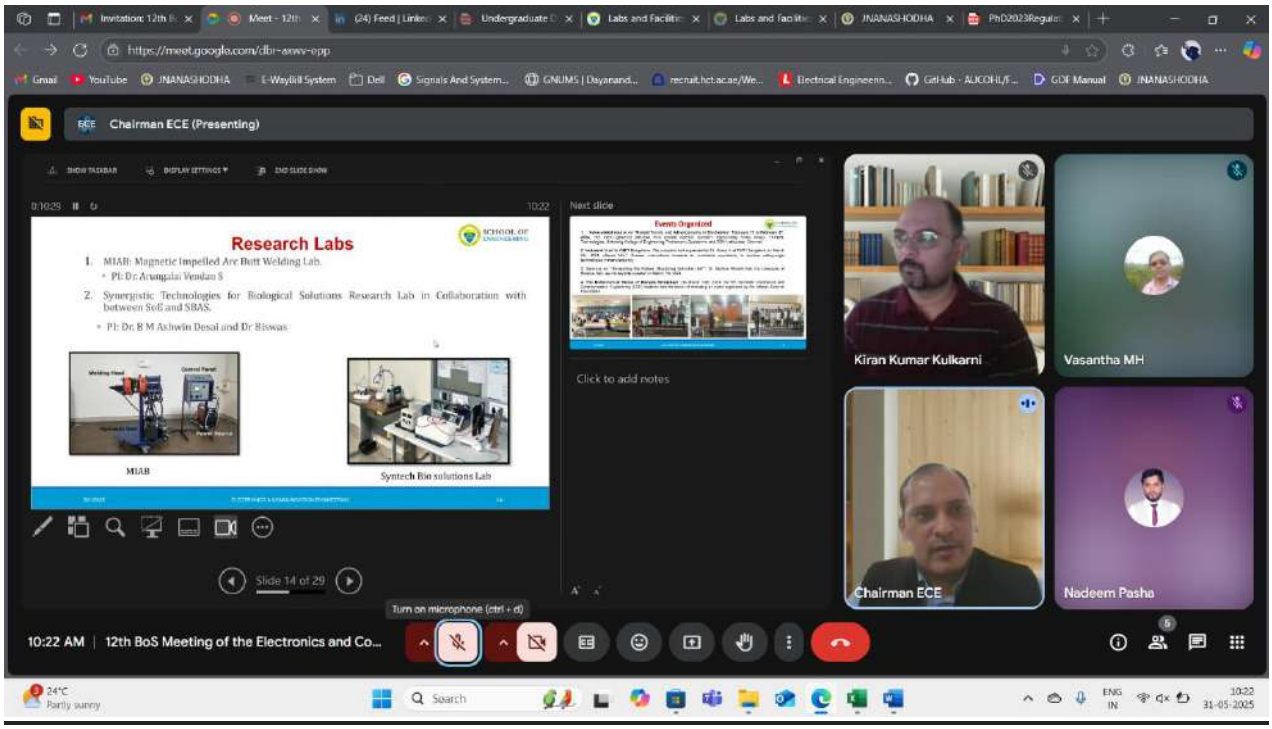
Sl. No.	Member	Suggestion	Action Taken by Department
1	**Mr. Kiran Kumar Kulkarni**	Enquired about implementation of the Skill Enhancement Course *"Introduction to Model-Based Design"* suggested in the 11th BoS meeting.	The course has been successfully implemented. Students are actively engaging in simulations and hands-on projects using model-based methodologies.
2	**Mr. Kiran Kumar Kulkarni**	Suggested integrating AI and ML courses with a methodological toolbox approach to equip students with practical implementation skills.	A new core course *"Machine Learning"* incorporating a tools-based approach is developed and will be offered in the upcoming academic year.
3	**Dr. Vasantha M. H.**	Recommended focusing on securing government-funded projects in collaboration with VLSI industries.	Faculty are being encouraged to initiate further collaboration with semiconductor industries.
4	**Dr. Vasantha M. H.**	Emphasized hands-on learning through practical projects including soldering, PCB design, circuit design, and simulation, ending with a mini prototype build.	A Skill Enhancement Course on PCB Design has been included. Courses are being restructured to include hardware mini-projects like LED dimmer circuits, basic amplifiers, etc., as part of internal assessments.
5	**Dr. Vasantha M. H.**	Suggested offering greater flexibility and resources to students and faculty to facilitate industry-oriented project work.	Extended lab hours and mentor support have been introduced. Industry project themes have been shared with final-year students. Discussions are underway with industry partners for collaborative projects.
6	**Dr. Vasantha M. H.**	Advised that electives and other courses be sequenced to ensure prerequisite knowledge is covered in earlier semesters.	The curriculum structure has been reviewed. Electives and core courses have been reorganized semester-wise based on prerequisite mapping. Implementation starts from the 2025–26 batch.
7	**Dr. Arungali Vendan**	Informed about the introduction of the core course *"Power Electronics and EV"* to promote sustainability and modern energy systems.	The course has been approved and integrated into the core curriculum from AY 2025–26. It includes topics on electric vehicles, power electronics, and renewable energy applications.

List of New Courses Introduced:

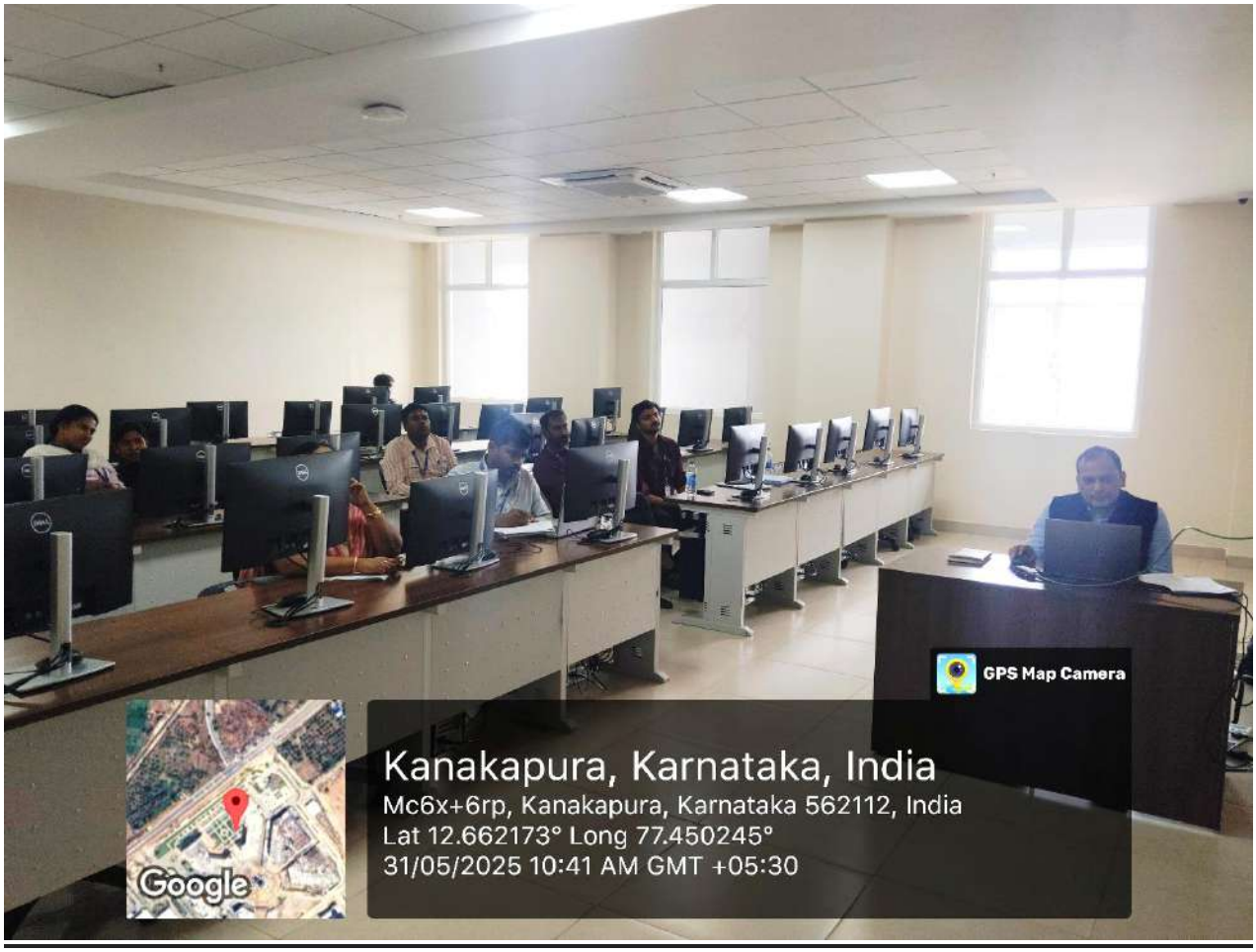
Course Type	Course Name	Teaching Hours / Week			
		Lecture	Tutorial	Practical	Project
		L	T	P	J
IPCC	Power Electronics and EV	3	0	2	0
PEC	AI and ML for VLSI CAD	3	0	0	0
PEC	Quantum Computing	3	0	0	0
PEC	5G Mobile Communication	3	0	0	0
PEC	Biomedical Signal Processing	3	0	0	0
PEC	Introduction to Deep Learning	3	0	0	0
PEC	Embedded Linux and Device Drivers	3	0	0	0
PEC	Nanoelectronics	3	0	0	0
PEC	Wearable Technology	3	0	0	0
PEC	Hardware accelerator for Artificial Intelligence	3	0	0	0
PEC	Principles of Soft Computing	3	0	0	0

Highlights from the BoS Session






SCHOOL OF ENGINEERING
NAAC
Department of Electronics & Communication Engineering
Welcomes
All BoS Members
to the
BoS Committee Meeting
on
31-05-2025


GPS Map Camera
Kanakapura, Karnataka, India
Mc6x+6rp, Kanakapura, Karnataka 562112, India
Lat 12.662173° Long 77.450245°
31/05/2025 10:41 AM GMT +05:30
Google