Minutes of 13th BOS

School	:	School of Engineering
College	:	Dayananda Sagar University
Programme	:	B.Tech-ECE
Department	:	Electronics and Communication Engineering
Mode of Meeting	:	Hybrid Mode
Meeting Link	:	https://meet.google.com/qqq-uoyd-wkj
Date and Time	:	15th May 2026, 10:00 AM
Venue	:	Lab A308, Dayananda Sagar University, Harohalli, Bengaluru South – 562112

AGENDA

The agenda for the BOS meeting is as follows:

I. Curriculum Development and Approval

B. Tech ECE Programmes:

1. Discussion on B.Tech ECE (2026–30 batch) – 1st to 8th Semester Scheme and Syllabus
2. Discussion on B.Tech ECE (2025–29 batch) – 3rd to 8th Semester Scheme and Syllabus
3. Discussion on B.Tech ECE (2024–28 batch) – 5th to 8th Semester Scheme and Syllabus
4. Discussion on B.Tech ECE (2023–27 batch) – 7th to 8th Semester Scheme and Syllabus

Academic Innovation:

5. Implementation of AI-integrated courses, including
    - Collaboration with NVIDIA
    - Curriculum integration with NVIDIA DLI
    - Integration pathways with NVIDIA AI Factory and frameworks
6. Any other academic matters

II. Doctoral Programs

- Discussion on the course syllabus for the department's PhD program

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III. Other Academic Matters

- Strategic goals and quality enhancement initiatives for the ECE Department
- Open forum for additional academic discussions.

MINUTES OF MEETING

The Board of Studies meeting of the Department of Electronics and Communication Engineering was convened on 15th May 2026 at 10:00 AM in Room A308, 3rd Floor, A Block, School of Engineering, Dayananda Sagar University, Harohalli Campus, Bengaluru. The meeting was conducted in hybrid mode to facilitate the participation of both in-person and remote members.

Dr. Arun Balodi welcomed all the BoS members and initiated the discussion regarding the newly introduced AI infrastructure at DSU in collaboration with NVIDIA.

The presentation highlighted the computational capability of the system featuring:

- 1.4 PetaFLOP FP8 performance with 160 GPUs
- Ability to fine-tune a 70-billion parameter Large Language Model (LLM) in under 6 hours
- Capability to simulate 100,000 robotic trajectories simultaneously using ISAAC Sim
- Support for 48 parallel student training jobs without resource conflict
- Real-time processing and analysis of large-scale cybersecurity logs
- Rendering of high-fidelity Omniverse environments for AR/VR research

Dr. Balodi emphasized the significance of this infrastructure in transforming DSU into an AI-driven research ecosystem and encouraged students and faculty to actively engage in interdisciplinary AI research.

Key discussions included:

- Utilization of GPU clusters for advanced ECE projects
- AI applications in embedded systems and communication networks
- Robotics simulation and automation research opportunities
- Cybersecurity analytics using AI acceleration
- AR/VR applications for immersive engineering education
- Industry collaboration and skill development aligned with emerging technologies

Faculty members discussed possible curriculum enhancements incorporating AI, machine learning, and high-performance computing modules within the ECE program.

Research scholars expressed interest in leveraging the infrastructure for:

- Deep learning model development
- Signal processing optimization
- Autonomous systems research
- Edge AI applications

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The meeting also focused on the implementation of the revised curriculum under the NEP 2020 framework with emphasis on industry-oriented learning, interdisciplinary education, and skill development within the ECE programs.

The discussion highlighted the significant growth in Scopus-indexed publications, citations, conference papers, books, and book chapters, along with improved publication quality through increased Q1 and Q2 journal contributions.

The Board reviewed the status of the funded research projects and noted that the projects undertaken by Dr. B.M. Ashwin Desai with support from DSU Seed Money and DST SERB SRG grants were successfully completed and formally closed.

The Board acknowledged the effective utilization of research grants and encouraged faculty members to continue pursuing externally funded interdisciplinary research projects and collaborative initiatives.

Dr. Arun Balodi highlighted that Dayananda Sagar University has entered into a landmark collaboration with NVIDIA to establish one of India's first AI-native research and learning ecosystems. The Board was informed about the advanced AI infrastructure comprising 20 NVIDIA DGX B200 nodes with 160 GPUs, designed to support large-scale AI model training, deep learning research, computer vision, natural language processing, and data-intensive analytics. Dr. Balodi explained that the facility provides students and faculty with exposure to industry-grade AI computing infrastructure used by leading global organizations. The discussion also covered DSU's AI-native learning ecosystem spanning the complete NVIDIA technology stack, including DGX B200 systems, Jetson edge AI devices, GPU workstations, NVLink communication, and software frameworks such as CUDA, cuDNN, PyTorch, TensorFlow, RAPIDS, and TensorRT. The Board appreciated the initiative and emphasized integrating AI-driven learning, research, and interdisciplinary applications into the ECE curriculum and research activities.

Research funding proposals submitted by the department were presented by Dr. Arun Balodi during the Board of Studies (BoS) meeting. A total of 16 proposals worth ₹725 Lakhs submitted to major funding agencies including DRDO, DST, ISRO RESPOND, ICMR, Quantum Mission, and SEED were highlighted. The proposals covered emerging research areas such as Artificial Intelligence, Quantum Computing, Biomedical Applications, Advanced Materials, Smart Healthcare Systems, and Space Research, reflecting the department's active involvement in interdisciplinary research and innovation activities.

Teaching–Learning and strategic academic initiatives of the Department of Electronics and Communication Engineering were presented during the Board of Studies (BoS) meeting by Dr. Arun Balodi. The presentation highlighted AI-integrated curriculum development, Outcome Based Education (OBE) practices, student skill development, industry-aligned learning, and continuous assessment methods. Strategic initiatives including research cluster formation, SIG activities, outreach programs, and Industry Curriculum Board activities were also discussed, with all initiatives actively progressing toward the department's academic and research goals for 2027.

Awards, recognitions, and key achievements of the Department of Electronics and Communication Engineering were presented during the Board of Studies (BoS) meeting by Dr. Arun Balodi. The presentation highlighted student and faculty achievements at various national

and international platforms, publication of books and research contributions, establishment of the Automotive Embedded Systems Lab in collaboration with industry partners, MoU activities, and recognition received by students and faculty members for academic, technical, and innovation excellence.

Placement, career development activities, and strategic collaborations of the Department of Electronics and Communication Engineering were presented during the Board of Studies (BoS) meeting by Dr. Arun Balodi. The presentation highlighted placement statistics, core placement growth, industry MoUs, and collaborations with organizations including INFAB-Bangalore, CeNSE IISc, IIT Roorkee, and MIT Square London to strengthen industry interaction, research, and skill development activities.

Dr. Arun Balodi presented the Graduation Outcomes – Result Analysis for the academic year 2024–25, highlighting the department's consistent academic performance across odd and even semesters.

The department also showcased various academic initiatives including AI foundations, CUDA-based GPU computing, embedded systems specialization, AI-integrated curriculum, and industry-focused learning practices.

Dr. Arun Balodi presented the curriculum design and programme credit framework of the B.Tech ECE programme, emphasizing employability, research & innovation, and entrepreneurship-oriented learning. The curriculum includes industry-relevant courses, internships, innovative projects, value-added courses, and academic flexibility through CBCS and NPTEL open electives. The BoS members appreciated the structured 160-credit framework spread across 8 semesters with focus on practical learning, design studios, and specialization verticals aligned with industry and higher education requirements.

Core ECE Foundations & Advanced Specialisation Courses: Dr. Arun Balodi presented the Semester III to VI curriculum structure focusing on core ECE foundations and advanced specialization courses. The curriculum includes subjects such as Analog Electronics, Digital Logic Design, Embedded Systems, CMOS VLSI Design, Signals and Systems, Machine Learning for Engineers, Wireless Communication, RF & Microwave Engineering, and Control Automation. It was highlighted that the Computer Organization and Architecture course incorporates NVIDIA CUDA Architecture concepts to strengthen GPU computing knowledge. The Design Studio – II course is designed as a project-based learning component to enhance practical problem-solving skills. It was also discussed that the Electrical Circuit Design course will be conducted as part of the Makers Space facility in B Block under NVIDIA-supported learning initiatives.

Dr. Arun Balodi presented the Semester VII and VIII curriculum highlighting capstone projects, internships, entrepreneurship, and industry-oriented professional elective verticals. The curriculum includes specialization tracks in Communication, Signal Processing, VLSI, IoT, Interdisciplinary Technologies, and NVIDIA DLI-based courses such as Deep Learning, Computer Vision, Generative AI & LLMs, and Robotics. The BoS members appreciated the research-driven and industry-aligned curriculum structure.

Dr. Arun Balodi presented the course structure comprising core courses and professional elective clusters in domains such as Industrial IoT, Automotive & Control, and Advanced Systems. The curriculum integrates industry-standard tools, NVIDIA DLI and NPTEL

certifications, research methodology, industrial internships, and project-based learning to strengthen industry and research alignment. The BoS members appreciated the focus on practical skills, research continuity, and emerging technology integration.

Dr. Arun Balodi presented the proposed Curriculum Strengthening Framework highlighting innovative teaching–learning approaches such as interactive lectures, hands-on projects, problem-based learning, group collaborative activities, MOOC integration, and industry guest lectures. He also explained the assessment framework comprising CIE and SEE components, along with the implementation of Bloom's Taxonomy levels and CO-PO mapping alignment using a 3-level attainment scale to enhance outcome-based education and academic quality.

Dr. Arun Balodi presented the resolutions placed before the Board of Studies for approval, including the adoption of the revised B.Tech ECE Scheme and semester-wise syllabus for 2026–30, M.Tech Embedded System Scheme and syllabus for 2026–28, ratification of NEP 2020 compliance, approval of elective verticals and MOOCs, introduction of the Minor Track, and adoption of the revised examination pattern. He further discussed the department's SWOC analysis, highlighting institutional strengths, opportunities for interdisciplinary growth and skill enhancement, key challenges, and areas requiring improvement for continuous academic and research excellence.

Dr. Arun Balodi presented the strategic roadmap for the ECE Department outlining short-term and long-term developmental goals for 2026, 2028, and 2030. The roadmap emphasized initiatives such as Live-in-Labs, makerspace-based upskilling, enhancement of research collaborations, achieving higher placement targets, establishment of Centres of Excellence in VLSI, Embedded Systems, and Energy Science, promotion of funded research and industry consultancy, calibration lab development for MSME collaboration, and strengthening international collaborations, global internships, innovation hubs, and placement partnerships to elevate the department's academic and research ecosystem.

Dr. Arun Balodi presented the proposed curriculum design and programme credit framework for the B.Tech ECE scheme 2026–30, emphasizing employability, research & innovation, and entrepreneurship-oriented learning. The curriculum includes employability skill development, industry-relevant courses, innovative projects, research and industry internships, management courses, Constitution of India (CIPE), and value-added courses. He explained the CBCS academic flexibility model with a total of 160 credits distributed across 8 semesters, incorporating 6 PEC verticals and 4 design studios. The semester-wise credit distribution was presented as 21 credits in Semester I, 19 in Semester II, 21 in Semester III, 23 in Semester IV, 22 in Semester V, 22 in Semester VI, 15 in Semester VII, and 17 in Semester VIII. The credit definition framework for lecture, tutorial, practical, and project components was also detailed to ensure clarity in curriculum implementation and outcome-based learning.

Dr. Arun Balodi presented the semester-wise credit distribution for the M.Tech Embedded Systems programme, detailing the curriculum structure across all four semesters. The presentation highlighted the allocation of credits for core subjects, electives, laboratory courses, MOOCs/NPTEL, seminars, internships, and dissertation phases, ensuring a balanced integration of theoretical knowledge, practical exposure, research methodology, and industry-oriented learning in alignment with NEP and outcome-based education practices.

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Dr. Udaya Kumar Reddy K R, Dean, School of Engineering, stated that the syllabus revision incorporating AI components was designed by considering curricula from global universities, IITs, and IISc. He also requested the BoS members to provide critical feedback and constructive suggestions for further improvement of the curriculum.

Dr. Udaya Kumar Reddy highlighted that the University campus is progressing towards a net-zero sustainability model in collaboration with Siemens, reflecting the institution's commitment to green technologies and sustainable development initiatives.

The Chairman presented various academic developments including the introduction of new courses, promotion of MOOC-based learning through platforms like NPTEL, and the development of advanced laboratories such as the Cadence Lab, MATLAB Lab, and Embedded Systems Lab (VECTOR Tool). These facilities are aimed at providing students with enhanced hands-on experience.

Dr. S. Arungalai Vendan stated that the number of research scholars has increased compared to the previous year, which is a positive sign for the department's research growth. He also highlighted the increase in research proposal submissions and mentioned that the University is supporting full-time research scholars through stipend provisions.

Dr. Arun Balodi informed the BoS members that certain courses in the B.Tech and M.Tech programmes, such as In-Vehicle Networks and Applied Model-Based Design, are being offered in collaboration with Tata Technologies as part of the MoU signed with the organization.

Conclusion

The shift to an AI-first academic ecosystem marks a major academic transformation at DSU. Through the collaboration with NVIDIA and the establishment of advanced AI infrastructure, DSU is creating a future-ready learning environment that bridges academics, research, and industry.

"The world is building AI. DSU is building the factory."

Prof. Nadeem Pasha of the department documented the proceedings and read out the minutes of the meeting to all members for confirmation and validation.

Dr. Arun Balodi, Chairman of the Department, concluded the meeting by proposing a vote of thanks. He expressed sincere gratitude to all the external and internal BoS members for their valuable suggestions, insights, and continued support towards the academic and strategic development of the department.

Meeting Adjourned: 1:30 PM



Suggestions:

Dr. Srinivas Talabattula, Associate Professor, Department of Electrical Communication Engineering, IISc, suggested aligning the syllabus with competitive examinations like GATE for higher studies and questioned the integration of core courses in the curriculum. Dr. Arun Balodi clarified that the scheme includes 58 credits of core courses, with only about 15% AI integration, ensuring strong focus on core ECE fundamentals.

Dr. Srinivas Talabattula enquired whether the proposed courses were aligned with curricula followed at institutions such as Stanford University and MIT. Dr. Arun Balodi clarified that, structurally, the proposed curriculum framework is designed on similar lines while being adapted to the requirements of the programme and NEP guidelines.

Dr. Srinivas Talabattula suggested the inclusion of courses related to Quantum Systems, Photonics, and Optical Communication Systems in the curriculum to address emerging technological advancements and research trends in the ECE domain.

Dr. Srinivas Talabattula enquired whether faculty members were being trained for the NVIDIA DLI courses included in the current curriculum. In response, Dr. S. Arungalai Vendan informed the Board that the University has planned Faculty Development Programmes (FDPs) during the month of July for faculty training, and the course fees exceeding 50 USD per course will be financially supported by the University.

Dr. Srinivas Talabattula suggested that analytical teaching methods should be emphasized for core subjects such as Signals and Systems, EMFT, and DSP by deriving and explaining equations on the board rather than relying predominantly on PowerPoint presentations, to strengthen students' conceptual understanding.

Dr. Srinivas Talabattula suggested introducing specialization-based programme titles in collaboration with NVIDIA, such as B.Tech in VLSI, to enhance industry relevance, branding, and student career opportunities.

Mr. Kiran Kumar Kulkarni, Global Industry Manager – Engineering and Technology Services, MathWorks India Pvt. Ltd., appreciated the proposed curriculum and noted that the inclusion of AI in courses related to healthcare technology development is a positive step aligned with current industry and technological advancements.

Ms. Rida Fatima, Alumni (DSU) and Supply Chain Engineer at BMW India Pvt. Ltd., appreciated the proposed curriculum for incorporating courses focused on net-zero initiatives and sustainability, highlighting their relevance to current industrial and environmental requirements.

Dr. Vasantha M H, Associate Professor, Department of ECE, NIT Goa, emphasized that the curriculum should maintain a balance between higher studies and industry requirements. He noted that industries are rapidly moving towards AI-based technologies, and incorporating AI-oriented courses is essential for the future survival and growth of students, universities, and industries.

Mr. Kiran Kumar Kulkarni enquired about the challenges faced in industry interaction and collaboration. Dr. Arun Balodi responded that bringing industry professionals into academics for teaching specific modules remains a major challenge. Mr. Kiran Kumar Kulkarni suggested identifying interested industry experts, noting that many professionals are willing to contribute towards academic activities and knowledge sharing.

Mr. Kiran Kumar Kulkarni appreciated the availability of industry-standard tools such as Cadence, MATLAB, Xilinx, and Vector in the department. He further suggested that greater emphasis should be placed on application-specific training and practical implementation to enhance industry readiness among students.



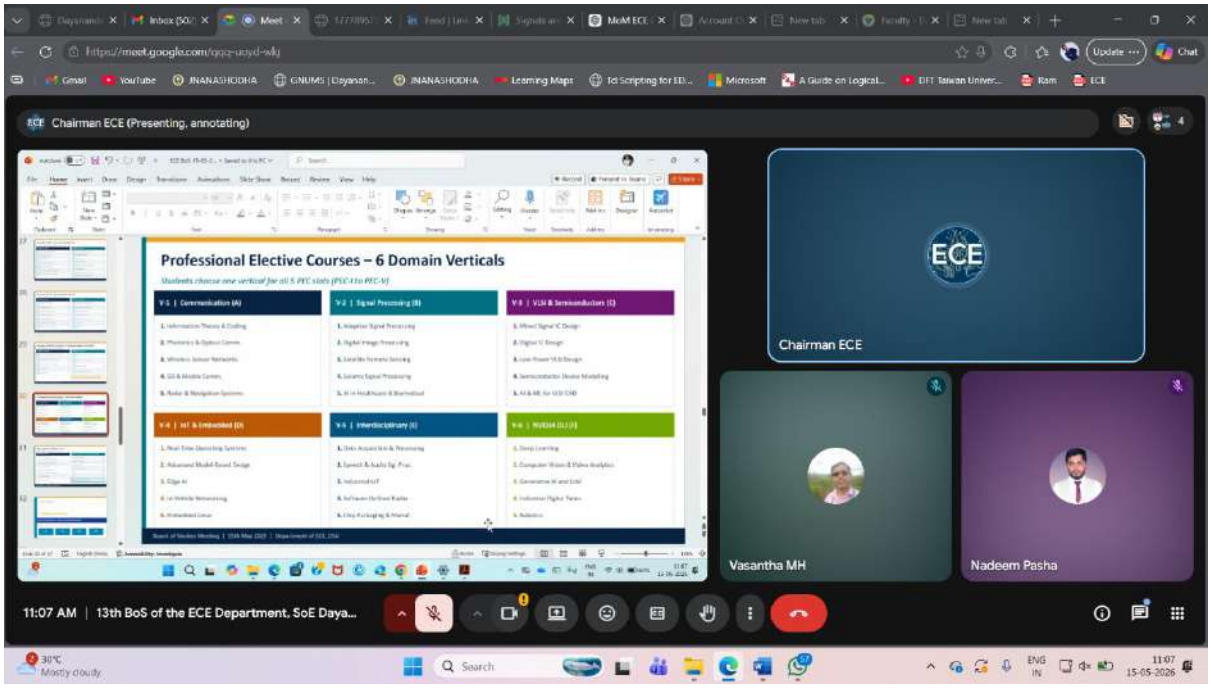
Approved by:
Dr. Arun Balodi
Professor and Chairman
Chairman, Board of Studies
Department of Electronics and Communication Engineering

Highlights from the BoS Session




GPS Map Camera
Bannikuppe, Karnataka, India
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Dept of Electronics & Communication Engineering
School of Engineering
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Programme	:	M.Tech- Embedded System
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I. Curriculum Development and Approval

1. **Postgraduate & Research Programmes:**
   - M.Tech ECE (2026–28 batch) – Scheme and Syllabus
2. **Academic Innovation:**

   Implementation of AI-integrated courses, including

   - Collaboration with NVIDIA
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II. Other Academic Matters

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MINUTES OF MEETING

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integration of theoretical knowledge, practical exposure, research methodology, and industry-oriented learning in alignment with NEP and outcome-based education practices.

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Conclusion

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Mr. Kiran Kumar Kulkarni enquired about the challenges faced in industry interaction and collaboration. Dr. Arun Balodi responded that bringing industry professionals into academics for teaching specific modules remains a major challenge. Mr. Kiran Kumar Kulkarni suggested identifying interested industry experts, noting that many professionals are willing to contribute towards academic activities and knowledge sharing.

Mr. Kiran Kumar Kulkarni appreciated the availability of industry-standard tools such as Cadence, MATLAB, Xilinx, and Vector in the department. He further suggested that greater emphasis should be placed on application-specific training and practical implementation to enhance industry readiness among students.



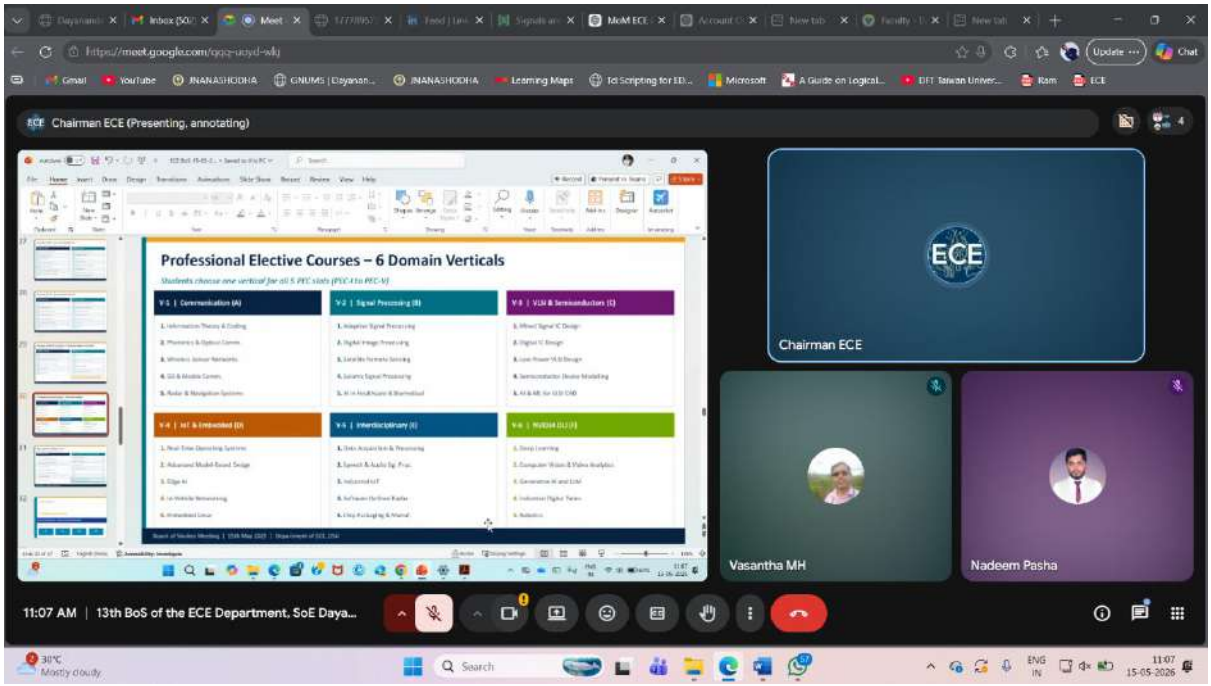
Approved by:
Dr. Arun Balodi
Professor and Chairman
Chairman, Board of Studies
Department of Electronics and Communication Engineering

Highlights from the BoS Session






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CHAIRPERSON
Dept of Electronics & Communication Engineering
School of Engineering
Dayananda Sagar University
Devarakaggalahalli, Kanakapura Road
Ramanagara District - 562112
Karnataka