



SCHOOL OF ENGINEERING

SOE-BULLETIN

The Official Newsletter of **School of Engineering**



SCHOOL OF ENGINEERING

Vision

Transform lives through excellence in engineering education, research and innovation with an emphasis on sustainability, inclusive technologies and global needs.

Mission

1. Design and deliver contemporary engineering curricula to address regional and global needs while emphasizing ethics, values, integrity and regional relevance.
2. Carry out high impact academic research, industry projects and innovation activities with active student engagement to advance science and engineering knowledge and state-of-the-art industry practices.
3. Develop regional and national leaders to advance the society and economy.

INDEX

CONTENTS	PAGE NO.
WORKSHOPS / SKILL DEVELOPMENT PROGRAMS	4
WEBINARS / SEMINARS / TECHNICAL TALKS	9
EVENTS: PROFESSIONAL SOCIETIES / CLUB ACTIVITIES	17
INDUSTRIAL VISIT	39
FACULTY ACHIEVEMENTS	47
STUDENT ACHIEVEMENTS	83



SCHOOL OF ENGINEERING



WORKSHOPS / SKILL DEVELOPMENT PROGRAMS

FIVE DAYS WORKSHOP ON “FAST PROTOTYPING OF DIGITAL SYSTEMS USING FPGA”

The Department of Electronics and Communication Engineering at Dayananda Sagar University organized a five-day workshop on "Fast Prototyping of Digital Systems using Verilog HDL" from 27th August to 31st August 2024. The workshop was meticulously designed to enhance the skills of students and professionals in the domain of digital system design using Verilog HDL, featuring a blend of lectures, hands-on sessions and demonstrations. Post the inauguration, an industry expert, Mr. Abhra Bagchi, Co-founder of fermion IC Design conducted an interactive lecture session focusing on the practical aspects of Verilog HDL. The session was highly appreciated by the participants for its industry-oriented insights. The five-day workshop was a resounding success, with participants gaining valuable insights and hands-on experience in fast prototyping digital systems using Verilog HDL. These sessions covered the design flow for FPGAs, including high-level design, simulation, synthesis and implementation. Participants were introduced to various design tools and software used in FPGA development, such as VHDL/Verilog and Xilinx Vivado. The event concluded with a valedictory session where participants were awarded certificates for their participation.

The poster features logos for the School of Engineering, IEEE DSU Student Branch, IEEE Signal Processing Society Bangalore Chapter, and Dayananda Sagar University. It includes a QR code for registration, a photo of the industry expert Mr. Abhra Bagchi, and lists of faculty, student coordinators, chief patrons, and conveners. The event is scheduled for August 27-31, 2024, at room A-309.

SCHOOL OF ENGINEERING **IEEE DSU Student Branch** **IEEE Signal Processing Society BANGALORE CHAPTER** **DAYANANDA SAGAR UNIVERSITY**

DAYANANDA SAGAR UNIVERSITY
DEVARAKAGGALAHALLI, HAROHALLI, KANAKAPURA RD, DIST. RAMANAGARA, KARNATAKA - 562112

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Organizing
WORKSHOP ON FAST PROTOTYPING OF DIGITAL SYSTEMS USING FPGA

To register, scan the QR

INDUSTRY EXPERT

Mr. Abhra Bagchi
Co-Founder, FermionIC Design

27 AUGUST-31 AUGUST
A-309

Chief Patrons:
Dr. D. HEMACHANDRA SAGAR, CHANCELLOR, DSU
Dr. D. PREMACHANDRA SAGAR, PRO CHANCELLOR, DSU

Patrons:
Dr. AMIT BHATT, VICE CHANCELLOR, DSU
Dr. PUTTAMADAPPA C. REGISTRAR, DSU
Dr. UDAYA KUMAR REDDY KR, DEAN-SOE, DSU

Convener:
Dr. ARUN BALODI, CHAIRMAN-ECE, DSU
Dr. SHIRSHENDU ROY

FACULTY CO-ORDINATORS:
PROF. SIVASANKARI S S
PROF. SUDHARSAN V

STUDENT CO-ORDINATORS:
CINDRELLA S- 8792532377
DEEKSHITHA R- 8431672282

SCHEDULE:

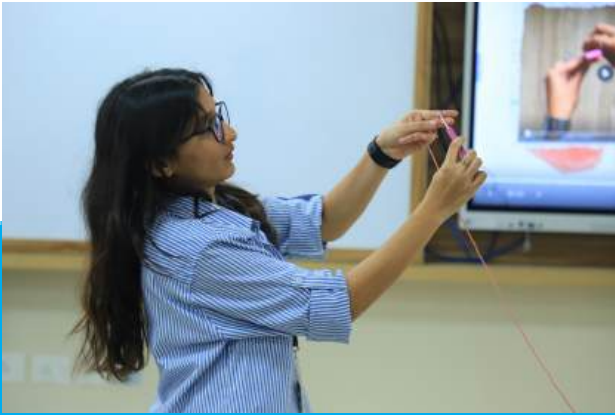
Date/Day	Session 1 (9:30 - 11:00 AM)	Session 2 (11:15 AM - 12.50 PM)	Session 3 (1:50 PM - 4.30 PM)
27/08/2024 Tuesday	Inauguration, talks by Dept. Chairman and other faculties. Lecture Hall 2, Block-A	Lecture session by Industry Expert Mr. Abhra Bagchi Lecture Hall 2, Block-A	Design and simulation of advanced digital blocks using Verilog. @A-309 Dr. S. Roy, Prof. Sivasankari
28/08/2024 Wednesday	Realizing all major blocks using Verilog HDL. Dr. S. Roy, Prof. Sivasankari	Training on advanced Verilog concepts Dr. S. Roy, Prof. Sudharsan	Realization of Memory Blocks using Verilog HDL Dr. S. Roy, Prof. Sivasankari
29/08/2024 Thursday	Instantiation and implementation of available FPGA based IPs. Dr. S. Roy, Prof. Sudharsan	Creation of native custom IPs in Vivado. Dr. S. Roy, Prof. Sivasankari	General Verilog coding techniques for IPs. Dr. S. Roy, Prof. Sudharsan
30/08/2024 Friday	Design and Implementation of FIR first order filter. Dr. S. Roy, Prof. Sivasankari	Design and Implementation of FIR first order filter. Dr. S. Roy, Prof. Sudharsan	Implementation and verification of FIR filter using ILA. Dr. S. Roy, Prof. Sudharsan
31/08/2024 Saturday	Interfacing protocols and their Verilog models. Dr. S. Roy, Student Speakers	Demo of Interfacing based experiments. Dr. S. Roy, Student Speakers	Training on System on Chip (SoC) Dr. S. Roy, Student Speakers Valedictory session



“Crochet Workshop”

The Department of Computer Science and Engineering (Cyber Security) has organized the “Crochet Workshop” an Inter-Departmental event on 26.09.2024. The workshop occurred in two sessions, from 9:00 AM - 12:00 PM and 1:00 PM - 4:00 PM, in the venue (Room No: A512). The event was enriched by a motivational address from the Chairperson, Dr. Durbadal Chattaraj, who inspired participants to explore new skills. It was open to both students and faculty and 80 spots were available (40 per session) on a first-come, first-served basis. Crochet supplies were provided to everyone so they could easily follow along and practice throughout the day. The main goal was to introduce everyone to the world of crochet, covering the basics of stitches and how to create beautiful handmade designs. The aim was to get participants to explore their creativity while crafting something unique from scratch. Each session welcomed 30 enthusiastic participants who eagerly engaged in hands-on learning. A highlight was the Chain Bash competition, where participants raced to create the longest chain in one minute, with winners receiving special crocheted prizes.







SCHOOL OF ENGINEERING



WEBINARS / SEMINARS / TECHNICAL TALKS

Alumni Talk Series Session-5: “The Spark that Ignited: How Research Fueled My Passion”

The Department of Computer Science and Engineering (CSE) at Dayananda Sagar University successfully organized the Alumni Talk Series Session 5 on 14th September 2024. The talk, titled “The Spark that Ignited: How Research Fueled My Passion,” was conducted targeting 5th and 7th-semester CSE students. The speaker for the session was Nyrika Bhargavaram Renuka, a distinguished alumna of the university and currently a Software Engineer II (AI/ML) at Lucid Motors. Nyrika holds an MS in Computer Science from Boston University and specializes in AI/ML applications in the automotive industry. Overall, "The Spark that Ignited: How Research Fueled My Passion" was an inspiring and motivating session.

SCHOOL OF ENGINEERING
DAYANANDA SAGAR UNIVERSITY
School of Engineering
Devarakaggalahalli, Harohalli, Kanakapura Road, Ramanagara Dist – 562 112.

Department of Computer Science and Engineering
Alumini Talk series
Session - 5

Topic : “The spark that ignited: how research fueled my passion”

Resource person

Objectives

- To motivate the youngsters to pursue higher studies
- To do research in multidisciplinary domains

Conveners

Dr. Udaya Kumar Reddy K R
Dean, SOE-DSU
Dr. Girisha G S
Chairman CSE-DSU

Alumni Coordinator

Dr. Gousia Thahaniyathi
Assistant Professor, CSE

Faculty Coordinator

Dr. Rajesh T M
Associate Professor, CSE

Date & Time: 14th Sep 2024 at 10:00 AM
Venue: Lecture Hall

Nyrika Bhargavaram Renuka
Software Engineer, (AI/ML) at Lucid Motors, USA



Webinar on “DevOps”

The Department of Computer Science and Engineering at Dayananda Sagar University successfully hosted a technical webinar on DevOps, focusing on continuous integration, deployment and test automation, on Wednesday, 11th September 2024. The session, conducted virtually via Google Meet, was targeted at M.Tech(CSE) 2nd-semester students, providing them with a valuable learning experience in one of the most sought-after technologies in the IT industry. The session was led by Dr. Selvaraj Kesavan, a Technical Architect at DXC Software Solutions, Bangalore. With his extensive experience and expertise in DevOps, Dr. Selvaraj provided the participants with insightful knowledge on how modern development teams are leveraging DevOps practices to streamline software development, deployment and testing processes. The event was coordinated by Dr. A. Senthil Kumar, Professor in the CSE Department at the School of Engineering, DSU. The webinar provided an excellent opportunity for students to understand the intricacies of DevOps and its application in the industry. The session received positive feedback, with students appreciating the practical insights shared by the resource person. Overall, the event was a successful step toward preparing students for the future challenges of the software industry.

Dayananda Sagar University
Devarakaggalahalli, Ramanagara Dt., Bengaluru - 562 112

 **SCHOOL OF ENGINEERING**

Department of Computer Science and Engineering
Cordially Invites you for an Technical Webinar
on
DevOps
FOR CONTINUOUS INTEGRATION,
DEPLOYMENT AND TEST AUTOMATION

Resource Person
 **Dr. Selvaraj Kesavan**
Technical Architect,
DXC Software Solutions, Bangalore

Conveners
Dr. Girisha G S **Dr. Udaya Kumar Reddy**
Chairman-CSE Dean,
School of Engineering, DSU School of Engineering, DSU

SEPTEMBER
WEDNESDAY | **11** | AT 9 AM
2024
GOOGLE MEET : [HTTPS://MEET.GOOGLE.COM/GOQ-WHXS-HNW](https://meet.google.com/GOQ-WHXS-HNW)

Coordinator
Dr. A. Senthil Kumar,
Professor, CSE Dept, School of
Engineering, DSU



Target Audience
M.Tech. CSE - II Sem

Tech Talk on the PCI-DSS 4.0 standards

Department of Computer Science and Engineering (Cyber Security) has organized a “Technical Talk”. SISA Information Security Pvt. Ltd. team consisting of three members visited to DSU Harohalli Campus on 24th September 2024 to provide a technical talk on the PCI-DSS 4.0 standards, full-time opportunities and internship availability as security tester and auditor in the same company. The Cysec Club of Dayananda Sagar University hosted an informative and engaging event in collaboration with SISA Information Security Pvt. Ltd. The event featured a line-up of esteemed delegates from SISA who shared invaluable insights into the field of cybersecurity, career opportunities and corporate culture. The primary goal of the event was to prepare students, particularly those in their final and pre-final years, for placement opportunities in the field of cybersecurity.



Seminar on: “Entrepreneurship and Innovation: Pathways to Dynamix Career”

On 26th September 2024, the Department of Computer Science & Technology hosted an insightful seminar titled “Expert Talk on Innovation & Entrepreneurship.” Dr. Bahubali Shriagapur, Professor, Dept. of AIML, Dayananda Sagar University shared his expertise on various aspects of entrepreneurship, technology and disruptive innovations.

The seminar started with the speaker's introduction by 5th sem student Pranati and Dr. Santosh Kumar J facilitated Dr. Bahubali Shriagapur with a plant. Then Dr. Bahubali Shriagapur handled the session he started his talk by giving information about himself and started explaining what is Entrepreneurship. He emphasized that entrepreneurs are visionaries who drive change and innovation. He also discussed the difference Between businessmen and entrepreneurs. Businessman: Typically focuses on existing business operations, profit and stability. Whereas Entrepreneur: Thrives on innovation, disruption and growth. The seminar delved into the different technology levels. The speaker highlighted essential requirements for successful entrepreneurship. He also shared his thoughts about why Entrepreneurship Matters for Computer Science and Engineering students. CST students play a vital role in shaping the future by leveraging innovation and entrepreneurship. In the end, he discussed the Disruptive Technologies that reshape industries.

The poster is for a seminar titled "Entrepreneurship and Innovation: Pathways to a Dynamic Career" organized by the Department of Computer Science & Technology at Dayananda Sagar University. It features a portrait of the speaker, Dr. Bahubali Shiragapur, and lists the seminar's objectives, outcomes, organizers, and patrons. The event is scheduled for September 26th, 2024, from 11:40 AM to 12:40 PM at venue A-325, B-Block, DSU.

DAYANANDA SAGAR UNIVERSITY
SCHOOL OF ENGINEERING
Devarakagalahalli, Harohalli, Kanakapura Rd, Dt. Ramanagara, Karnataka-562112

Seminar on
"Entrepreneurship and Innovation: Pathways to a Dynamic Career"

Organized by
Department of Computer Science & Technology

Dr. Bahubali Shiragapur,
Professor, Department of AIML,
Dayananda Sagar University

Date: 26TH SEP
Time: 11:40 AM-12:40PM
Venue: A-325, B-Block, DSU

Objectives

- To familiarize participants with the core concepts of entrepreneurship and innovation, and their role in shaping dynamic career paths.
- To cultivate an entrepreneurial mindset by exploring how innovative thinking can lead to the identification of opportunities and the creation of impactful ventures.
- To expose participants to various career paths within entrepreneurship and innovation.

Outcomes

- Participants will have a clear understanding of entrepreneurship and innovation, including their importance in today's business landscape.
- Participants will have a clearer understanding of how entrepreneurship and innovation can serve as a dynamic career pathway.
- Participants will be able to brainstorm and formulate innovative business ideas and develop a basic roadmap for converting those ideas into viable ventures.

Patrons

Dr. D. Hemachandra Sagar
Chancellor, DSU

Dr. D. Premachandra Sagar
Pro-Chancellor, DSU

Shri. Gallswamy
Secretary, DSU

Dr. Amit R Bhatt
Vice-Chancellor, DSU

Dr. Puttamadappa C.
Registrar, DSU

Dr. Uday Kumar Reddy
Dean- SOE

Dr. Naveen Babu
Associate Dean- SOE

Organizer
Dr. D. Sudha
Assoc. Professor, CST
Prof. Ramandeep Kaur
Assistant Professor, CST

Chairperson
Dr. M. Shahina parveen
Professor, CST



Expert talk on Innovate with Confidence: “Tackling Microsoft AI Skill Challenges”

Department of Computer Science and Technology arranged an expert talk on “Innovate with Confidence: Tackling Microsoft AI Skill Challenges” on 27th September 2024 from 11:00 AM to 12:00 PM for 5th-semester students. The session was delivered by Dr. Gomathi S., Microsoft Most Valuable Professional through online mode. This session helped students to equip themselves with AI skills and exciting roles in technology and engage in practical knowledge through interactive challenges.

SCHOOL OF ENGINEERING
Bennuragallikatti, Harohalli, Kankagere Rd, Dr. Rameshwaran,
Harohalli 562112

**Expert Talk on
“Innovate with Confidence: Tackling Microsoft AI Skill Challenges”**

**Organized by
Department of Computer Science & Technology**

Dr. Gomathi S.
Microsoft Most Valuable Professional

Objectives

- Participants will equip students with in-demand AI skills.
- Program students for exciting roles in technology.
- Engage in practical knowledge through innovative challenges.

Patrons

Dr. B. Praveenrajendra Sagar
Chairman, DSU

Dr. B. Praveenrajendra Sagar
Pro-Chancellor, DSU

Dr. Anil R. Bhat
Vice-Chancellor, DSU

Dr. Prabhu Sagar C.
Registrar, DSU

Dr. Rajyashree Reddy
Dean, SCE

Dr. Vasavi Babu
Associate Dean- SCE

Organizer

Dr. Sangeetha Kumar J.
Asstt. Professor, CTT
Prof. Nivedha N. S. C.
Assistant Professor, ASU

Chairperson

Dr. H. Lakshminarayana
Professor, CTT

Date: 27/9/24
Time: 11:00 AM-12:00 PM
Venue: DSU

Objectives

- To facilitate participants with get to know AI concepts and its uses.
- To provide a comprehensive overview of machine learning, AI key concepts, and practical use cases.
- Develop skills in data analysis, visualization, and interpretation using basic ML techniques relevant to Data AI and Data Science.



Invited Talk on "Touching Lives While Touching the Moon"

The Department of Aerospace Engineering and the Centre for Space Science and Technology (CSST) organized an invited talk on India's illustrious space journey under the theme "Touching Lives While Touching the Moon: India's Space Saga." The event featured distinguished guests, including Mr. N. Viswanatha, former Group Director of the Spacecraft Mechanisms Group at the UR Rao Satellite Centre, ISRO, who delivered an insightful lecture. The talk was highlighted by inspiring speeches and a tribute to the pioneers of the space industry.





SCHOOL OF ENGINEERING



EVENTS: PROFESSIONAL SOCIETIES / CLUB ACTIVITIES

“DevHack 2024: National Hackathon”

On 27th and 28th September 2024, Dayananda Sagar University hosted its much-anticipated hackathon, DevHack, a 36-hour intensive event that brought together some of the brightest minds in technology from across India. With a total prize pool of ₹3 lakhs and a cash prize of ₹2 lakhs, the hackathon provided a unique platform for students to develop cutting-edge solutions to real-world challenges.

DevHack 2024 provided a stimulating environment that spurred innovation, teamwork and problem-solving. Participants teamed up to tackle a wide range of industry issues, pushing the boundaries of modern technology and presenting innovative solutions. The hackathon, recognized nationally for its intellectual rigor and competitive spirit, was open to students from across India, bringing together young technologists and future innovators.



The poster for DevHack 2024 features a central image of a globe with a network overlay. Text on the poster includes: DAYANANDA SAGAR UNIVERSITY, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING PRESENTS, DEVHACK 36 hours hackathon, Venue: Lecture Hall 2, PRIZE POOL 3 LAKH, CASH PRIZE 2 LAKH, MINI EVENTS, SWAGS AND GOODIES, 27th-28th September 2024, HYBRID HACKATHON: Hack from home or on site, Faculty Coordinators: Dr. Bipin Kumar Rai, Professor, CSE; Dr. Meenakshi Mathotra, Asso. Professor, CSE; Student Coordinators: Patel Muhammad (+91 8154852286), Utkarsh Priya (+91 9939635206), Ritvik Vasundh (+91 8299696568), and a QR code. Logos for AICTE, IEEE, ACM, and the Institution's Innovation Council are also present.



“Internal Hackathon: A Step Towards SIH 2024 Excellence”

The School of Engineering at Dayananda Sagar University, through its Department of Computer Science & Engineering, organized a one-day Internal Hackathon on 6th September 2024. The event, held at A Block Lecture Hall 2, ran from 9:00 AM and followed the guidelines set by the Smart India Hackathon (SIH) 2024 committee. It was a platform designed to prepare and nurture innovation among the registered teams for the upcoming national SIH competition.

The primary objective of the internal hackathon was to foster collaborative problem-solving and technological innovation within the university. Students from various branches of engineering, including Electronics and Communication Engineering (ECE), Computer Science and Engineering (CSE), Artificial Intelligence and Machine Learning (AI & ML), Data Science (DS), Computer Science and Technology (CST), Aerospace Engineering, Cyber Security and Mechanical Engineering, were encouraged to participate. A total of 37 teams, each consisting of six members, including at least one female participant, registered for the event. The hackathon allowed students to work on real-world problems, focusing on selected problem statements and themes from the SIH portal.

The poster features logos for the Ministry of Education, MIE's Innovation Cell, and Smart India Hackathon 2024. It prominently displays the university and school names, along with the department and event title. Key details include the SPOC (Dr. Basavaraj N Hiremath), conveners (Dr. Udaya Kumar Reddy K R and Dr. Girisha G S), coordinators (Dr. Souitha Hiremath and Prof. Nandini K), and a list of jury members (Dr. Hanumanth Sastry Sistla, Smt Sneha Dabbadi Lakshminpathi, Dr. Rupam Bhaduri, Dr. Nagaraja S R, and Vasu M Deshpande). The event is scheduled for 9:00 am onwards on 06-09-2024 at LH 2, Block A. Social media icons for DSU are also present.

DAYANANDA SAGAR UNIVERSITY
SCHOOL OF ENGINEERING
DEVARAKAGGALAHALLI, HARIHALLI, KANAKAPURA ROAD,
RAMANAGARA DIST-562 112
DEPARTMENT OF COMPUTER SCIENCE
AND ENGINEERING
Smart India Hackathon 2024

SPOC:
Dr. Basavaraj N Hiremath
Professor, CSE, SOE, DSU

Conveners:
Dr. Udaya Kumar Reddy K R
Dean-SOE, DSU
Dr. Girisha G S
Professor and Chairman, CSE, SOE, DSU

Internal Hackathon 2024

Time: 9:00 am onwards
Date: 06-09-2024
Venue: LH 2, Block A

Coordinators
Dr. Souitha Hiremath
Associate Professor, CSE, SOE, DSU
Prof. Nandini K
Assistant Professor, CSE, SOE, DSU

Jury Members

Dr. Hanumanth Sastry Sistla
Professor in Department of
Computer Science Engineering
(Artificial Intelligence (AI) and
Machine Learning (ML))

**Smt Sneha Dabbadi
Lakshminpathi**
Delivery Manager at UST GLOBAL,
PRESTIGE SHANTINKETAN, Bangalore

Dr. Rupam Bhaduri
Professor in Department of AI &
Robotics

Dr. Nagaraja S R
Professor & Chairman in the
Department of Aerospace Engineering

Vasu M Deshpande
CEO at Easylib Software Pvt. Ltd.
and Finvenys Technologies Pvt. Ltd.

“Inaugural Ceremony of IEEE Information Theory Society Student Chapter”

The Inaugural ceremony of the IEEE Information Theory Society (ITS) Student Chapter was held on September 23, 2024, at 10:00 AM at the CDSIMER Auditorium, located on the F-Block Ground Floor. This highly anticipated event was organized by the Department of Computer Science and Engineering(Data Science), School of Engineering, in collaboration with the IEEE Information Theory Society (ITS). The ceremony aimed to officially launch the IEEE ITS Student Chapter at DSU, providing a platform for students and faculty to engage with one of the most prestigious professional societies in the realm of information theory and technology.

The ceremony was led by key organizers, including Dr. Shaila S G, Chairperson of the CSE - Data Science, Dr. Pavan Kumar U, Faculty Advisor IEEE ITS Student Branch, DSU and Prof. Sindhu A, Faculty Coordinator of the IEEE ITS Student Branch at DSU. The event was further supported by the leadership of Dr. Pushpa Mala S, the IEEE Student Branch Counselor and Dr. Arun Balodi, Faculty Advisor of IEEE SPS and MTTs, DSU. The student committee played an integral role in managing the event, with Nitin Prajwal R, Chair of IEEE ITS, Pavan Kumar G, Vice-Chair, Janardhan KS, Secretary and other IEEE ITS members taking the lead in student coordination.

Their guidance and support were instrumental in the successful launch of the IEEE ITS Student Chapter, further enhancing the academic standing of DSU.

IEEE BANGALORE SECTION
IEEE DSU
SCHOOL OF ENGINEERING
DEPARTMENT OF CSE-DATA SCIENCE
INVITES YOU TO

Inauguration
CEREMONY
OF
IEEE INFORMATION THEORY SOCIETY
STUDENT CHAPTER

SEPTEMBER **23** 10:00 AM
CDSIMER BLOCK
LECTURE HALL

We look forward to your gracious presence

ABOUT IEEE
The Institute of Electrical and Electronics Engineers (IEEE), founded in 1963, is a leading global professional association for engineers, scientists, and professionals in electrical, electronic, and computer engineering, as well as related fields. With members in over 160 countries, IEEE advances technological innovation through influential journals, conferences, and standards development, playing a key role in integrating new technologies into society.

ABOUT IEEE ITS
The IEEE Information Theory Society is a top professional body dedicated to advancing the core mathematical principles of information technology to enhance human life. It explores the fundamental aspects of how information is processed, transmitted, stored, and utilized, providing the basis for efficient communication and data handling. By exploring these fundamental concepts, the society drives innovation and improves technology. Its work supports advancements in various fields, enhancing data handling and communication processes.

CHIEF PATRONS:
Dr. D. Hemachandra Sagar -
Chancellor, DSU
Dr. D. Premachandra Sagar - Pro
Chancellor, DSU

PATRONS:
Dr. Anil B Bhat - Vice Chancellor,
DSU
Dr. Putnamajappa C - Registrar, DSU
Dr. Udaya Kumar Reddy KH - Dean of
SRM
Dr. Ramroth B. Galgolkere - Dean
(Acad) - Science & Technology
Dr. Anurag G - Dean Research
Dr. Navene Babu - Associate Dean of
SRM

CONVENERS:
Dr. Shash S G - Chairperson CSE-Data
Science, DSU
Dr. Poojya Mala S - IEEE Student Branch
Coordinator, Chair of IEEE, DSU
Dr. Arun Rajesh - Faculty Advisor of IEEE
SRM and MITSS, DSU
Dr. Pavan Kumar U - Faculty Advisor IEEE
ITS Student Branch, DSU

FACULTY CO-ORDINATORS:
Prof. Sandhu A - Faculty Co-ordinator IEEE
ITS Student Branch, DSU

STUDENT COMMITTEE:
Nitin Prajwal R - Chair of IEEE ITS, DSU
Pavan Kumar G - Vice Chair of IEEE ITS,
DSU

EVENT SCHEDULE

Contact Us

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Speaker



DR. PREMANANDA B S
SENIOR MEMBER, IEEE
IEEE EXECOM FOR IEEE ITS BANGALORE CHAPTER
ASSOCIATE PROFESSOR, RVCE, BANGALORE.



“Teacher’s Day 2024 Celebration- Felicitation Program”

On September 5th, Teacher's Day 2024, the Department of AI-ML honored the dedication and contributions of educators through a heartwarming celebration. Hosts Ritvik and Neha welcomed the audience, setting a cheerful tone for the day. The event began with an inspiring speech by Dr. Jayavrinda Vrindavanam V, Chairperson, followed by recognition of outstanding academic achievements. It started with the Dean of Academics, Dr. Ramesh R Galigekere and Associate Dean, Dr. Naveen Babu delivering a speech on teachers’ day. Faculty members, including Dr. Joshuva Arockia Dhanraj, Dr. Monika Goyal, Dr. Shreyas Rajendra Hole, Prof. Subhash Mondal, Prof. Pradeep Kumar K, Prof. Sriramkumar R, Prof. Jeevaraj R, shared insights into their recent research and publications. Each educator received applause and a memento as a token of appreciation. The event's appeal was enhanced by 5th Semester students, Deekshitha and Swathi’s mesmerizing song performances. In a special gesture, the students gifted their teachers, expressing gratitude for their relentless efforts. The celebration concluded with a cake-cutting ceremony and heartfelt thanks from the hosts, highlighting the importance of appreciating teachers’ day.



“Engineer the Future” organized by Yantrove Club”

The Yantrove Club, under the Department of Computer Science and Engineering (AI & ML), hosted an ideathon titled "Engineer the Future" on September 18, 2024, to celebrate Engineers' Day. The event gathered 19 teams to propose innovative solutions to four key themes: Ethics in AI, Engineering for Multi-Planetary Species, Space Debris Mitigation and Education Technology. After a welcome address by Chairperson Dr. Jayavrinda Vrindavanam, participants had two and a half hours to develop their ideas. The competition featured two evaluation rounds, with five teams advancing to the final stage. Judges assessed the teams based on innovation, functionality, impact, scalability and presentation. The top three teams were awarded prizes, with the first-place team receiving Rs. 1000. The event concluded with a speech by Prof. Subhash Mondal, who commended the participants for their creativity and problem-solving abilities, encouraging them to continue innovating in their engineering journeys.



“Appreciation & Felicitating with Trophies for 4th semester Toppers of Batch 2022-2026”

The Department of Computer Science and Engineering (Artificial Intelligence & Machine Learning) organized a special event to honor the top-performing students of the 4th semester, Batch 2022-2026. Ms. Lakshya U Reddy (ENG22AM0169) and Mr. Trijal R (ENG22AM0167) secured a perfect 10 CGPA, while Mr. Yogesh N (ENG22AM0070) and Mr. Harshith G R (ENG22AM0021) achieved a 9.58 CGPA. The Head of the Department congratulated them for their outstanding achievements and presented them with trophies, encouraging all students to strive for excellence in their academic journey.



“Web development competition Web Wizard - Halloween Edition”

Department of Computer Science and Engineering (Cyber Security) has organized a Web development competition named “Web Wizard - Halloween Edition”, through the IEEE Communications Society Student Branch DSU, which was designed to highlight participants' creativity and technical skills in web development, with a Halloween theme as the central focus. Competitors created fully functional websites incorporating spooky aesthetics and Halloween-related content. To enhance the challenge, new tasks were introduced every 30 minutes, requiring participants to integrate features such as login pages, registration forms and additional navigation elements. This dynamic environment fostered innovation and collaboration among participants while allowing them to showcase their technical prowess. The event featured esteemed guests and judges who contributed to the competitive atmosphere. Dr. Naveen Babu, Associate Dean, SoE served as the Chief Guest, while Dr. Pushpa Mala S, Associate Professor, Councilor, IEEE Student Branch was the distinguished Guest. The judging panel included Prof. Deepak H. G and Dr. Basavaraj N Hiremath, Professor, Dept. of CSE both of whom evaluated the participants' web development projects, focusing on creativity, functionality and adherence to the Halloween theme. Their expertise added significant value to the competition.



“Teachers Day Celebrations” - CSE(DS)

The Teachers Day celebration at the Department of CSE(Data Science) was held with great enthusiasm to honor and appreciate the hard work and dedication of our teachers. The event was organized by Dr. Shaila S G, Professor & Chairperson attended by all faculties and staff members. Teachers’ Day is celebrated annually on 5th September, marking the birth anniversary of Dr. Sarvepalli Radhakrishnan, a respected scholar and the second President of India.

The celebration began at 12:30 PM with a warm welcome by the head of the department CSE (Data Science), who expressed gratitude to the teachers for their tireless efforts in shaping the future of the students.

The Teachers Day celebration was a resounding success, with everyone's joy and a sense of gratitude. The event not only highlighted the importance of teachers but also strengthened the bond between all the teachers, making it a day to remember.



“The Logo League - IEEE Information Theory Society”

The Logo League competition, organized on September 23, 2024, at 2:00 pm by the Department of Computer Science and Engineering(Data Science), in collaboration with the IEEE Information Theory Society (ITS) Student Chapter, was held at Dayananda Sagar University (DSU), A Block, Room A405. The event aimed to foster student creativity through the design of a logo that represents the IEEE ITS Student Chapter.

The Logo League was an event designed to encourage students to participate in the creative process by designing a logo that aligns with the mission and vision of the IEEE Information Theory Society. The event drew students from various disciplines, creating a space for innovation, creativity and design thinking. The IEEE ITS Student Chapter provided participants with all the necessary tools and materials to successfully produce their designs, offering them a glimpse into the world of branding and logo creation. The event witnessed a good turnout, with students and faculty members actively involved throughout the competition.

Participants were then briefed on the rules of the logo design contest, after which they began working on their entries. The competition was followed by the judging process, where the entries were evaluated based on several criteria, including creativity, relevance and alignment with the society’s goals.



“Onam Celebration” – CST

The Department of Computer Science and Technology at Dayananda Sagar University organized a vibrant Onam celebration. The event brought together faculty, staff and students to celebrate the rich cultural heritage of Kerala. The venue was adorned with traditional decorations, including the intricate Pookolam (floral arrangement), adding to the festive atmosphere. Faculty members and students actively participated in the celebration. The event fostered a spirit of unity, joy and cultural appreciation within the department. Everyone enjoyed the day, marking it as a memorable occasion for all.



“ Teamwork, Empowering Leadership and Mental Well-being on occasion of Teacher’s Day”

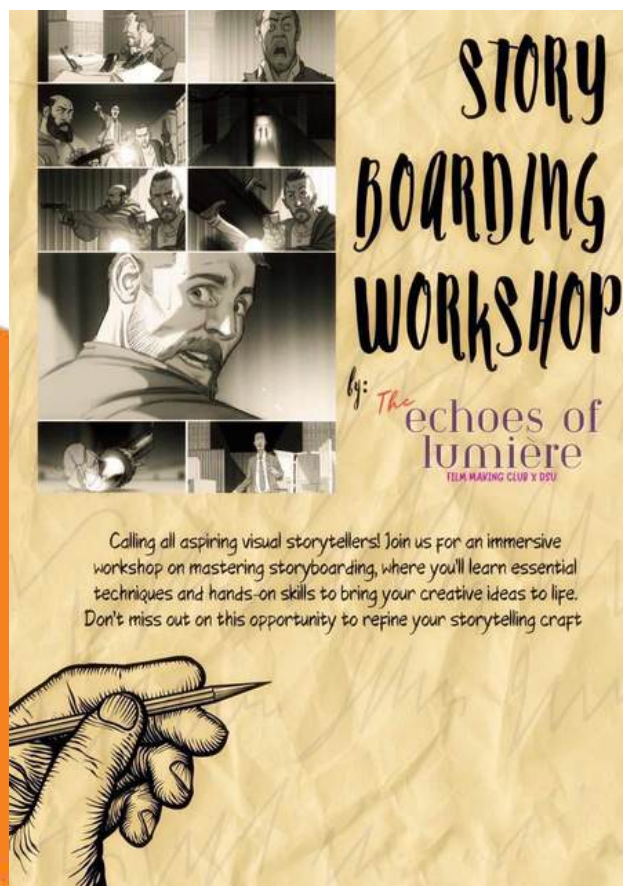
The Department of Computer Science & Technology celebrated Teacher’s Day on 5th September 2024 with great enthusiasm, paying tribute to the dedication and hard work of educators who shape the future. This year, the celebration went beyond mere recognition, delving into themes of teamwork, empowering leadership and mental well-being—values that are increasingly crucial in the evolving landscape of education.

Teamwork was highlighted as a key ingredient for achieving academic success and fostering a positive learning environment. Through collaborative efforts among teachers, administrators and students, schools and universities are better equipped to deliver innovative and impactful education. The event featured several group activities where teachers demonstrated how teamwork allows for better problem-solving, resource sharing and the development of a supportive community. A panel discussion was held to highlight how teamwork amongst faculty members fosters a more cohesive learning experience. Teachers shared personal experiences about how interdisciplinary teamwork has led to better educational outcomes. On Teacher’s Day, the focus was on leadership that inspires others to excel and take ownership of their roles. The mental well-being of both educators and students was a central theme of the event. Acknowledging the challenges teachers face, the discussion focused on mental health resources and practices that can support them. These sessions provided teachers with strategies for managing stress, work-life balance and student mental health challenges. Experts highlighted the importance of mindfulness, regular mental health check-ins and creating a supportive environment in the workplace.



“The Echoes of Lumière, SOE”

On 20th September 2024, from 2:30 PM to 4:20 PM, the Echoes of Lumière, Filmmaking Club, SOE organized a successful storyboarding workshop in front of the School of Engineering (SOE) main building. The purpose of the event was to acquaint the incoming students with the craft of visual storytelling and the fundamentals of storyboarding, an important phase in the creation of films and animation. The workshop attracted enthusiastic students, eager to explore the creative process behind storytelling. Our core members, Ananya (2nd Year) and Nischal (2nd Year) led interactive sessions where they showed participants how to convert concepts into a series of drawings that illustrate a story's narrative flow. Working on their own storyboards, participants were offered the guidance and recommendations of the club's core members. The engaging atmosphere and hands-on approach helped participants develop their illustrating competencies and enhanced their better understanding of how to break down complex ideas into manageable visual scenes. The workshop ended in a positive tone and was a great success, encouraging students to collaborate and express their creativity practically.





“Pre-Placement training: Corporate Culture”

The DataScience@DSU Club, the Department of CSE (Data Science) organized Pre-Placement session on Pre-Placement: Corporate Culture held on 27th September 2024, from 09:00 AM to 04:00 PM in A441,SOE, DSU, organized by Dr. Shaila S G, Professor and Chairperson (DS), Prof. Shivamma D, Assistant Professor, Dept. of CSE (Data Science) and Prof. Monish L, Assistant Professor, Dept. of CSE (DataScience). More than 50+ students have been registered and participated in the event.

The poster is for a pre-placement training session titled "Pre-Placement: Corporate Culture". It is organized by the Department of Computer Science and Engineering (Data Science) at Dayananda Sagar University, in association with the DataScience@DSU Club. The session is held on 27.09.2024 from 9:00 am to 4:00 pm at the T-441 'A' Block, SOE, DSU. The resource person is Ritu, a CIL Trainer at SOE, DSU. The conveners are Dr. Amit Bhatt (Vice Chancellor, DSU), Dr. Udaya Kumar Reddy KR (Dean, SOE), Dr. G. Narveen Babu (Assoc. Dean, SOE), and Dr. Shaila S G (Chairperson, Dept. of CSE (DS)). The organizers are Prof. Shivamma D (Assistant Professor, Dept. of CSE(DS)) and Prof. Monish L (Assistant Professor, Dept. of CSE(DS)). The target audience is 5th Semester B Tech, CSE (DS) Students.

School of Engineering
Devarakaggalahalli, Harohalli, Kanakapura Road Ramanagara Dt., Bengaluru - 562 112

Department of Computer Science and Engineering (Data Science)
In association with **DataScience@DSU Club**
Is Organising
A Session by Placement Cell

Pre-Placement: Corporate Culture

Resource Person:
Roopa Priya J K
Placement Officer
SOE, DSU
Ritu
Trainer, CIL
SOE, DSU

Date: 27.09.2024 at 9:00 am to 4:00 pm
Venue: T-441 'A' Block, SOE, DSU

Conveners:
Dr. Amit Bhatt
Vice Chancellor, DSU
Dr. Udaya Kumar Reddy KR
Dean, SOE
Dr. G. Narveen Babu
Assoc. Dean, SOE
Dr. Shaila S G
Chairperson, Dept. of CSE (DS)

Organizers:
Prof. Shivamma D
Assistant Professor
Dept. of CSE(DS)
Prof. Monish L
Assistant Professor
Dept. of CSE(DS)

Target Audience : 5th Semester B Tech, CSE (DS) Students



MATLAB DAY & Workshop on “Unlocking Creativity: MATLAB for the Future”

The DataScience@DSU Club, the Department of CSE (Data Science) & IEEE student Chapter-Information Theory Society organized MATLAB DAY & Workshop on "Unlocking Creativity: MATLAB for the Future." held on 25th September 2024, from 09:30 AM to 04:00 PM in F block Auditorium, DSU, organized by Dr. Shaila S G, Professor and Chairperson (DS), Prof. Shivamma D, Assistant Professor, Dept. of CSE (Data Science) and Prof. Monish L, Assistant Professor, Dept. of CSE (Data Science). More than 100+ students have been registered and participated in the event.

DAYANANDA SAGAR UNIVERSITY
Devarakaggalahalli, Harohalli, Kanakapura Road Ramanagara Dt., Bengaluru - 562 112

School of Engineering
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING (DATA SCIENCE)

In association with
DataScience@DSU Club and MathWorks
Is Organising
MATLAB DAY
ON "UNLOCKING CREATIVITY: MATLAB FOR FUTURE ENGINEERS"

Date:
25th September, 2024
@ 9:00 am to 4:00 pm

Resource Person:

- Mr. Avinash Application Engineer, MathWorks Bangalore
- Mr. Rakshith B S Senior Application Engineer, MathWorks, Bangalore

Conveners:

- Dr. Amit Bhatt Vice Chancellor, DSU
- Dr. Udaya Kumar Reddy KR Dean, SoE
- G Naveen Babu Assoc. Dean, SOE
- Dr. Shaila S G Chairperson, Dept. of CSE (DS)

Organizers:

- Prof. Shivamma D Assistant Professor Dept. of CSE (DS)
- Prof. Monish L Assistant Professor Dept. of CSE (DS)

Student Co-ordinators:

- Nitin Prajwal R
- Jayashree K S
- Sujeeth Kumar
- Pavan Kumar

Venue:
CDSIMER Hospital Auditorium, F Block, Ground Floor, DSU

Register Here



“Semicon India 2024”

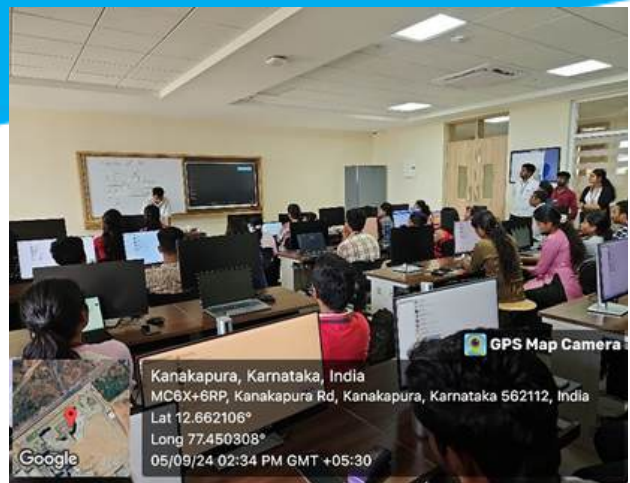
Semicon India 2024 is a three-day event aimed at showcasing India's semiconductor strategy and policies, with the goal of establishing the country as a global hub for semiconductor manufacturing. Prime Minister Narendra Modi on Wednesday said that 'now' is the right time to be in India while addressing a gathering at the Semicon India 2024 event at the India Expo Mart in Uttar Pradesh's Greater Noida. Dayananda Sagar University, School of Engineering (DSU-SOE), facilitated the participation of its students and faculty members in the prestigious SEMICON India 2024 event through Video Conference (VC). The event provided an excellent platform for participants to gain insights into the latest innovations, trends and advancements in the semiconductor industry, aligning with DSU's mission to promote academic excellence and industry exposure. Dr. Arun Balodi addressed students of the ECE Department on the occasion of the inaugural session of SEMICON India 2024 on 11 September. 'In the 21st century of Bharat, the chips are never down. All the students attended the address given by the Hon'ble Prime Minister Shri Narendra Modi ji. Propelling India's semiconductor industry growth is exploding demand for smartphone, automotive and data storage innovations.



“PCB Designing Workshop Using KiCad”

ELECTROBLITZ Club Under the Department of Electronics and Communication Engineering of Dayananda Sagar University conducted the event “PCB Designing Workshop Using KiCad” on 5th and 6th September 2024 from 2:00 PM to 4:00 PM which was conducted on campus. The event took place under the guidance of Mrs DIVYASHREE HB, Convener, ELECTROBLITZ CLUB. The event hosts were Ms. AARTHI NAYAK ULLAL (PRESIDENT) and Mr.PRAMATH GOPAL BHAT(VICE PRESIDENT) of the ELECTROBLITZ CLUB.

The event “PCB Designing Workshop Using KiCad” was conducted with the insight of providing the participants with knowledge of KiCad PCB designing software and intricate circuitry.



SDP-Hands on Workshop on “Communication Systems and Antenna Design”

ELECTROBLITZ Club Under the Department of Electronics and Communication Engineering of Dayananda Sagar University conducted the event “SDP-Hands on session on Communication Systems and Antenna Design” from 12th to 14th September 2024 from 10:00 AM to 1:00 PM which was conducted on-campus. The event took place under the guidance of Dr DIVYASHREE HB, Convener, ELECTROBLITZ CLUB and Dr. Supraja Eduduru, Assistant Professor, Dept Of ECE. The event hosts were Ms. AARTHI NAYAK ULLAL (PRESIDENT) and Mr. PRAMATH GOPAL BHAT (VICE PRESIDENT) of the ELECTROBLITZ CLUB. The three-day workshop commenced with the esteemed presence of Dr. Amit Bhatt, Vice Chancellor of DSU, Dr. Naveen Babu, Associate Dean of DSU-SOE and Dr. Arun Balodi, HOD of ECE.

DAYANANDA SAGAR UNIVERSITY

MTT-S
IEEE MICROWAVE THEORY & TECHNOLOGY SOCIETY

A+
NAAC

ELECTROBLITZ CLUB PRESENTS

STUDENT DEVELOPMENT PROGRAM

12-09-2024 to 14-09-2024

HANDS ON SESSION ON COMMUNICATION SYSTEMS & ANTENNA DESIGN USING MATLAB

Organized By

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Dr. Devaraju R
ASSOCIATE PROFESSOR
DAYANANDA SAGAR COLLEGE OF ENGINEERING
BENGALURU

Dr. Nanda Kumar M
ASSISTANT PROFESSOR
SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY
YAMAMPET, HYDERABAD

CHIEF PATRONS:
DR. D. HEMACHANDRA SAGAR, CHANCELLOR, DSU
DR. D. PREMACHANDRA SAGAR, PRO CHANCELLOR, DSU

PATRONS
DR. AMIT BHATT, VICE CHANCELLOR, DSU
DR. PUTTAMADAPPA C, REGISTRAR, DSU
DR. UDAYA KUMARA REDDY K R
DEAN, SOE, DSU

CONVENORS:
DR. ARUN BALODI, CHAIRPERSON
ECE, DSU

FACULTY CO-ORDINATORS
DR. DIVYASHREE H B
DR. SUPRAJA EDURU

STUDENT CO-ORDINATORS
NANDEESH : 7676871986
SHREERAKSHA: 7022455909





SCHOOL OF ENGINEERING



INDUSTRIAL VISIT

“Industrial Visit to IBM ”

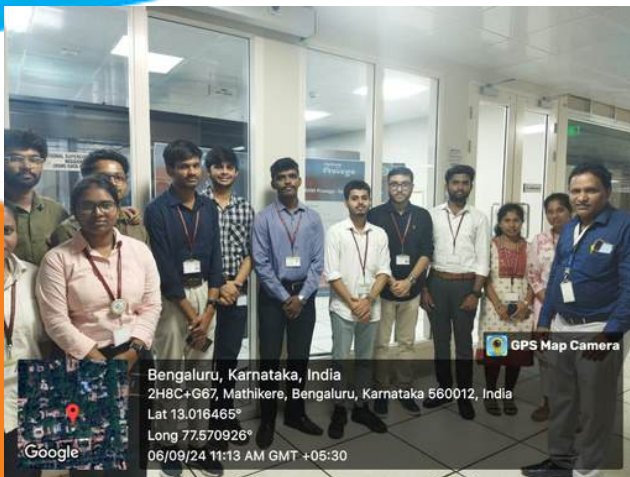
The Department of Computer Science Engineering at Dayananda Sagar University successfully organized an industry visit to IBM on 5th September 2024. The visit aimed to expose faculty members to ongoing research projects and innovations in cutting-edge fields like Artificial Intelligence (AI), Cloud Computing, Quantum Computing and Cybersecurity. The primary goal of the industry visit was to observe IBM's ongoing projects in key technology areas such as AI, cloud computing, quantum computing and cybersecurity. This experience provided valuable exposure for faculty members, inspiring new academic and career perspectives for both them and their students. The visit allowed the faculty to interact with IBM researchers and engineers, gain practical insights into these technologies, explore collaboration opportunities and network with experts in the field. Faculty members had the opportunity to interact with IBM experts to discuss the industry's future trends and opportunities.



“Educational Visit to IISc ”

On 6th September 2024, the Department of Computer Science and Engineering (CSE) at Dayananda Sagar University organized an educational visit for the 5th-semester students to the Supercomputer Education and Research Centre (SERC) at the Indian Institute of Science (IISc), Bangalore. This visit was part of the department’s initiative to expose students to advanced research facilities and cutting-edge technology in the field of supercomputing and computational science.

The primary goal of the visit was to provide the students with a first-hand experience of supercomputing technology and its applications in solving real-world computational challenges. By interacting with experts and witnessing the facilities, students gained insight into how high-performance computing is used to tackle complex scientific and engineering problems. The educational visit to SERC, IISc was a valuable and enriching experience for the 5th-semester students. It gave them a deeper understanding of supercomputing and its pivotal role in modern research and development. The Department of CSE looks forward to organizing more such visits in the future to foster a spirit of inquiry and innovation among its students.



“Industrial Visit to UR Rao Satellite Centre (URSC)”

The 5th-semester AIML students of Dayananda Sagar University had the opportunity to visit the prestigious UR Rao Satellite Centre (URSC), Bengaluru, which provided them with valuable insights into India’s space technology.

The visit began with Mrs. Umavathi Madam introducing Dr. Udupi Ramachandra Rao, the Father of Satellite Technology in India and his contributions, including the Aryabhata satellite. Students were then shown the Clean Room, where satellite components are meticulously assembled and tested, ensuring optimal performance. They learned about India’s launch vehicles, PSLV and GSLV and satellite orbits like LEO, MEO and GEO.



Industry Visit to “Hewlett Packard Enterprise for Data Center /Technology”

Department of CSE (Data Science) Faculties Dr. Shaila S G, Professor and Chairperson, Dr. U. Pavan Kumar attended an Industry Visit to Hewlett Packard Enterprise, Bengaluru for Data Center/Technology Workshop on 27th September 2024. Niraj Kumar (Client Architecture) from Hewlett Packard Enterprise gave a brief idea about setting up the High-Performance Computing Lab at Dayananda Sagar University, Harohalli. The primary objective of the visit was to gain insights into Data Center technology adopted by HPE. The HPE Team took us to their research lab and Data centers. The Team discussed the HPC set-up, Equipment, Floor planning, Usage, Projects and innovations. Additionally, the visit aimed to explore potential academic-industry collaborations and gain a deeper understanding of the technologies shaping the future workforce. A visit to HPE was a valuable opportunity to explore cutting-edge Data Center technology and gain insights into High-Performance Computing (HPC) setups. The discussions with the HPE team highlighted the importance of academic-industry collaboration in shaping the future workforce. This collaboration promises to enhance educational outcomes for faculty and students alike, positioning us at the forefront of technological advancements in the field.



“Industrial Visit on Product Development”

On September 25, 2024, the students of the CST department at DSU conducted an industrial visit to the Harohalli Industrial area. This visit aimed to gain practical insights into industrial operations, understand the application of theoretical knowledge in a real-world setting and interact with managers and workers to identify problem statements and automation problems. The visit to the Harohalli industry area was a valuable experience for the CST students. It bridged the gap between theoretical knowledge and practical application, enriching their understanding of industrial operations. The insights gained will be beneficial for their future careers.



The flyer is a rectangular document with a light green background. At the top, it features logos for Davanagere Sagar University, A+ NAAC, and the School of Engineering. The main title is 'Industrial Visit' in red. Below it, it states 'Organized by Department of Computer Science & Technology'. The date is '25TH SEP', time is '01:00 PM AM-04:00PM', and venue is 'Harohalli Industrial Area'. The 'Objectives' section lists three points: providing insights into theoretical concepts, interacting with industry professionals, and introducing cutting-edge technologies. The 'Outcomes' section lists three points: gaining problem-solving skills, collaborating with industry experts, and implementing new technologies. The 'Patrons' section lists six individuals: Dr. D. Hemachandra Sagar (Chancellor, DSU), Dr. D. Premachandra Sagar (Pro-Chancellor, DSU), Shri. Galiswamy (Secretary, DSU), Dr. Amit R Bhatt (Vice-Chancellor, DSU), Dr. Puttamadappa C (Registrar, DSU), Dr. Uday Kumar Reddy (Dean- SOE), and Dr. Naveen Babu (Associate Dean- SOE). The 'Organizer' section lists three individuals: Dr. Santosh Kumar J (Assoc. Professor, CST), Prof. Chithambarathanu (Assistant Professor, CST), and Prof. Vinayaka V M (Assistant Professor, CST). The 'Chairperson' section lists one individual: Dr. M. Shahina parveen (Professor, CST).

SCHOOL OF ENGINEERING
Devarakagalahalli, Harohalli, Kanakapura Rd, Dt. Ramanagara,
Karnataka-562112

Industrial Visit

Organized by
Department of Computer Science & Technology

Date: 25TH SEP
Time: 01:00 PM AM-04:00PM
Venue : Harohalli Industrial Area

Objectives

- Provide students with insights into how theoretical concepts learned in the classroom are applied in real-world industrial environments.
- Interact with industry professional to get real world problems.
- Introduce students to cutting-edge technologies, tools, and software solutions utilized by industry professionals.

Outcomes

- Students will gain problem-solving skills by applying academic concepts to industry-relevant challenges.
- Students will have opportunities to collaborate with industry experts, gaining exposure to real-world issues and case studies.
- Implement new technologies in projects, fostering adaptability and continuous learning to keep pace with technological advancements.

Patrons

Dr. D. Hemachandra Sagar
Chancellor, DSU

Dr. D. Premachandra Sagar
Pro-Chancellor, DSU

Shri. Galiswamy
Secretary, DSU

Dr. Amit R Bhatt
Vice-Chancellor, DSU

Dr. Puttamadappa C
Registrar, DSU

Dr. Uday Kumar Reddy
Dean- SOE

Dr. Naveen Babu
Associate Dean- SOE

Organizer

Dr. Santosh Kumar J
Assoc. Professor, CST

Prof. Chithambarathanu
Assistant Professor, CST

Prof. Vinayaka V M
Assistant Professor, CST

Chairperson

Dr. M. Shahina parveen
Professor, CST



“Bangalore Space EXPO 2024”

The students in the third year of Aerospace Engineering had the opportunity to visit the Bangalore Expo on September 20, 2024. Students were exposed to the newest breakthroughs in aircraft technology and learned about the intriguing projects on display.

Novel spacecraft designs and cutting-edge propulsion technologies piqued everyone’s interest. It was an excellent opportunity to network with like-minded people and receive insight into the future of space exploration.





SCHOOL OF ENGINEERING



FACULTY ACHIEVEMENTS



Prof. Sasikala N
Assistant Professor
Department of CSE

- Prof. Sasikala N, Assistant Professor, Department of CSE, served as a reviewer for the “Springer 7th International Conference on Intelligent Computing and Communication (ICICC-2024)” organized by the Department of Computer Science and Engineering, CMR Technical Campus, Hyderabad, Telangana, on the 30th and 31st of August, 2024. Her immense contributions to ICICC-2024 were instrumental in bringing out quality papers in the Springer Lecture Notes in Networks and Systems (LNNS) Series, ISSN 2367-3389.



- Prof. Sasikala N., Assistant Professor, Department of CSE, presented a paper titled “AI-Enhanced Farming: A Real-Time Monitoring App Powered by IoT” at the IEEE 5th International Conference on Smart Electronics and Communication (ICOSEC-2024), organized by Kongunadu College of Engineering and Technology, Trichy, Tamil Nadu, India, from 18th to 20th September 2024.





Prof. Mala B. A.
Assistant Professor
Department of CSE

- Prof. Mala B. A., Assistant Professor, Department of CSE, successfully completed an online course on the Scopus Academy module “Basic Search” on 5th September 2024.





**Dr. Savitha Hiremath,
Associate Professor
Department of CSE**

- Dr. Savitha Hiremath, Associate Professor, Department of CSE, extended her service as a member of the Advisory Committee for the 3-day Faculty Development Program on “Inculcating Best Research Practices” held in August 2024 at Vivekananda College of Engineering and Technology, Puttur, Dakshina Kannada, Karnataka.





**Dr. Gokulakrishnan S,
Assistant Professor,
Department of CSE**

- Dr.Gokulakrishnan S., Assistant Professor, Department of Computer Science and Engineering, successfully completed a Faculty Development Program on “MERN Stack Web Development” conducted by Microsoft India and SAP India under the Tech Saksham Program from 20th to 24th August 2024.
- Dr. Gokulakrishnan S., Assistant Professor, Department of Computer Science and Engineering, has successfully completed the 5-day Faculty Development Program on Building Web Development Solutions with Cloud Computing under the Next Gen Employability Program, held from August 27 to August 31, 2024.





Dr. Natarajan Venkateswaran
Professor of Practice
Department of CSE

- Dr. Natarajan Venkateswaran, Professor of Practice, Department of CSE, delivered an invited talk on "Artificial Intelligence Applications in Science and Engineering" at DSCE on 18th September 2024. The talk was organized by the Chemical Engineers Association (a student body) of the Chemical Engineering Department and was very well received and appreciated.

DAYANANDA SAGAR COLLEGE OF ENGINEERING
(Autonomous Institute Affiliated To VTU, Belagavi, Approved By AICTE & ISO 9001:2015 Certified) Accredited By
National Assessment & Accreditation Council (NAAC) With An 'A' Grade

DEPARTMENT OF CHEMICAL ENGINEERING
INAUGURATION OF CHEMICAL ENGINEERING
ASSOCIATION (ChEA) 2024-25
GUEST TALK ON
"ARTIFICIAL INTELLIGENCE APPLICATIONS IN SCIENCE AND
ENGINEERING"

Dr Natarajan Venkateswaran
Professor of Practice
Computer Science and Engineering,
Dayananda Sagar University

Seminar Hall
Mechanical Building, DSCE

11:00 A.M.

18 SEPTEMBER, 2024

Dr. G.K Mahadevaraju
Assistant professor
PRESIDENT - ChEA

Dr. Ravishankar R
HOD

Dr. B.G Prasad
PRINCIPAL, DSCE

Nikhil Sagar J
Shravya Devadiga
VICE PRESIDENTS

Poster by: Shravya Devadiga, 4th year





**Dr. T. Poongodi,
Professor,
Department of CSE**

- Dr. T. Poongodi, Professor, Department of CSE, will serve as a Guest Editor for the BMC Medical Informatics and Decision-Making journal, under the special issue on "Sensor Informatics and Disease Prediction," published by Springer Nature. This is an SCI Journal [Q1] with an Impact Factor of 3.3.



BMC Medical Informatics and Decision Making

Call for Papers

Sensor informatics and disease prediction



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BMC Medical Informatics and Decision Making is calling for submissions to our Collection on Sensor informatics and disease prediction. This collection welcomes submissions on novel developments in the field of sensor informatics, for disease prevention and health risk assessment, early disease detection, and monitoring of chronic diseases. Any technical advancement for the acquisition, processing, use, and retrieval of wearable sensor-collected biomedical data will be also considered.

Guest Editor

- Fazlullah Khan, PhD, Department of Computer Science, Faculty of Science and Engineering, University of Nottingham Ningbo, China
- Imran Razzak, PhD, School of Computer Science and Engineering, University of New South Wales, Australia
- T Poongodi, PhD, Department of Computer Science and Engineering, School of Engineering, Dayananda Sagar University, Bangalore, India
- Varun G Menon, PhD, SCMS School of Engineering and Technology, India



www.biomedcentral.com/collections/sidp
Submission Deadline: 31 October 2024



Part of Springer Nature

- Dr. T. Poongodi, Professor, Department of CSE, will serve as the Editor for the Scopus-indexed book “Green Federated Learning for Sustainable Smart Computing,” published by John Wiley & Sons – Scrivener.



Green Federated Learning for Sustainable Smart Computing

About the book:

The rapid growth of machine and deep learning resources has led to a significant increase in energy consumption due to the expanding volume of data required for training and computation. This has generated an urgent need for sustainable solutions in smart computing. While numerous books focus on machine learning, deep learning, and federated learning, most primarily address their introduction, characteristics, privacy, and applications. However, there is a noticeable gap in the literature concerning energy efficiency, sustainability, and the environmental impact of these technologies. The proposed book, “Green Federated Learning for Sustainable Smart Computing,” aims to fill this gap by providing a comprehensive perspective on the implementation of green federated learning techniques in smart computing. It will focus on optimizing energy consumption while maintaining learning model quality and addressing the crucial issues related to energy consumption and environmental sustainability.

The main topics of interest for this book include, but are not limited to:

1. Green Federated Learning Concepts and its Applications
2. Energy Consumption Analysis in Federated Learning
3. Sustainable Key Performance Indicators for Green Federated Learning
4. A Comprehensive Framework for Green Federated Learning
5. Client Selection Criteria and Strategies in Green Federated Learning
6. Designing a Sustainable Incentive System in Federated Learning
7. Knowledge Transfer Methods and Optimization in Green Federated Learning
8. Sustainable Aggregation Techniques for Green Federated Learning
9. Data Management and Optimization Strategies in Federated Learning
10. Reducing and Optimizing Communication Rounds in Federated Learning
11. Decentralized and Collaborative Optimization Techniques in Federated Learning
12. Edge and Wireless Optimization for Sustainable Federated Learning
13. An Integrated Application Framework for Sustainable Federated Learning



Important Dates:

Abstract Submission (of approx. 500 words): 5 Oct 2024	Abstract Acceptance: 10 Oct 2024
Full Chapter Submission: 15 Jan 2025	Chapter Acceptance: 10 Feb 2025
Final Camera-Ready Submission (in Word): 15 Mar 2025	Submissions to Publishers: 25 Mar 2025

How to Submit Your Chapter:

Send your 500 words abstract by the designated deadline to: gflssc2024@gmail.com

Note: Full chapter length: 8000-120000 words. Make sure to list all authors with complete affiliation details. The publisher's guidelines can be located at <https://scrivenerpublishing.com/guidelines.php>. Note that all chapters will be put through similarity software, and the publisher's guidelines require an overall similarity index of less than 8% (with a maximum of 1% from any single source). The editor(s) will engage 2 single blind peer-reviewers to assess originality, clarity, usefulness, and adherence to scope of project.

Editor(s):

Prof (Dr) Pao-Ann Hsiung, Dept. of Computer Science and Info. Engineering, National Chung Cheng University, Chiayi, Taiwan.

Dr. John A, Associate Professor, Dept. of Informatics, Faculty of communication Engineering, Military College of Telecommunication and Engineering, Mhow, Indore, MP, India.

Dr. T Poongodi, Professor, Dept. of CSE, School of Engineering, Dayananda Sagar University, Bangalore, Karnataka, India.

Dr. M Kiruthika, Assistant Professor, Dept. of Computational Intelligence, School of Computing, SRM Institute of Science and Technology, Kattankulathur, Tamil Nadu, India.

The book will be published under the Wiley-Scrivener imprint and will be indexed by Scopus and offered to Web of Science.

- Dr. T. Poongodi, Professor, Department of CSE, will serve as the Editor for the Scopus-indexed book “Intelligent Manufacturing Systems: Digitalization of Industrial Production,” published by CRC Press, Taylor & Francis Group.



CRC Press
Taylor & Francis Group



CALL FOR BOOK CHAPTERS

Intelligent Manufacturing Systems: Digitalization of Industrial Production

We are delighted to extend an invitation to contribute a good quality chapter for the upcoming edited book with CRC Press Taylor & Francis Group.

Chapter 1. Origin and development of intelligent manufacturing
Chapter 2. Framework for designing intelligent manufacturing systems
Chapter 3. Data-driven paradigm for intelligent manufacturing
Chapter 4. Big data in Intelligent Manufacturing
Chapter 5. Deep learning for intelligent manufacturing
Chapter 6. AI and ML techniques for manufacturing domain
Chapter 7. Development of a cloud-based intelligent manufacturing system
Chapter 8. Augmented Reality Applications in Intelligent Manufacturing
Chapter 9. Internet of Things (IoT) for Intelligent Manufacturing Systems
Chapter 10. Industrial Internet of Things and Cyber Manufacturing Systems
Chapter 11. Blockchain in Intelligent Manufacturing Systems
Chapter 12. Digital twin for Manufacturing
Chapter 13. 3D and 4D printing: Innovative process engineering
Chapter 14. Robotics in Manufacturing

Authors are invited to submit chapter proposals (around 200-250 words) stating the objective and structure of the unpublished research work along with table of contents to editedbookims@gmail.com. Further guidelines (template, references style, format etc.) will be shared upon provisional acceptance of the proposal. The final accepted chapters will be indexed in Scopus.

Note:- Only Quality and Original papers are accepted. Plagiarism of the chapter should be less than 10%. **Use of AI Tools is Prohibited** Figures should be of high quality and original.

Editors:

Dr. Pravin Kumar Singh

Department of Mechanical Engineering, Galgotias University, Greater Noida, Uttar Pradesh, India

Dr. P. Suresh

Department of Mechanical Engineering, Galgotias University, Greater Noida, Uttar Pradesh, India

Dr. T. Poongodi

Department of Computer Science and Engineering, School of Engineering, Dayananda Sagar University, Bangalore, Karnataka, India.

Dr. R. Gopal

Information and Communication Engineering, College of Engineering, University of Buraimi, Oman.

Dr. P. Senthil

Department of Engineering, MIE Section, University of Technology and Applied Sciences, Muscat, Oman

Important Dates:

Proposal Submission Deadline:

5th October 2024

Decision on Proposal:

8th October 2024

Full Chapter Submission:

15th December 2024

Revision Notification:

26th December 2024

Revised Full chapter Submission:

15th January 2025

Final Acceptance Notification:

20th January 2025

For More Info (Contact Details): +918299719940, +9181305 68273. Email ID: editedbookims@gmail.com

- Dr. T. Poongodi, Professor, Department of CSE, will serve as the session chair for the Scopus-indexed International Conference, specifically for the special session entitled "Federated Learning for Real-Time IoMT-based Edge Computing," at the International Conference on Machine Learning and Data Engineering (ICMLDE 2024), held from 28th to 29th November 2024 at UPES, Dehradun, India.

CALL FOR PAPERS



3rd International Conference on Machine Learning and Data Engineering (ICMLDE, 2024)

<http://www.icmlde.org>

28-29th November 2024

at

UPES, Dehradun, India

In association with

University of Dayton, USA

&

Providence University, Taiwan

All accepted papers will be published in **Procedia Computer Science Journal, Elsevier (Scopus Indexed)**

<https://www.journals.elsevier.com/procedia-computer-science>

We wish to provide a forum for focused discussions on new topics or innovative applications of established approaches in various fields of Machine Learning and Data Engineering. For this, we are inviting papers from researchers/academicians/scholars/ industry experts from highly reputed organizations.



ICMLDE, 2024 accepted paper will be included by Elsevier in the open-access Procedia Computer Science Journal.

<https://www.journals.elsevier.com/procedia-computer-science/>

Procedia Computer Science is indexed in Scopus, the Web of Science, INSPEC, and Engineering Village. All published papers in Procedia Computer Science are freely available on the Elsevier content platform ScienceDirect (www.sciencedirect.com).

Procedia Computer Science has an h-index of 140 and it has a CiteScore of 4.5.

Kindly visit the conference website at <https://www.icmlde.org> for more details.

ICMLDE 2022 received more than one thousand manuscripts worldwide. A very large number of high-quality submissions were received from them 273 were accepted for publication in Procedia Computer Science Journal, Elsevier. All papers have been published in Procedia Computer Science, Elsevier, Vol 218, 2023.

ICMLDE 2023 garnered significant attention, with more than 1000 submissions from over 3,500 contributors spanning across the globe. The submissions were from 45 countries, including India, United States, Russia, Oman, Sri Lanka, Hungary, Nigeria, Saudi Arabia, Viet Nam, Bangladesh, Canada, UK, Malaysia, Australia, Thailand, Ethiopia, Morocco, Norway, Philippines, Spain, Turkey, South Africa, Tunisia, China, France, Zimbabwe, Germany, Cyprus, Czechia, Poland, UAE, Algeria, Finland, Iraq, Netherlands, Saint Barthelemy, Bahrain, Ecuador, Jordan, Peru, Singapore, Switzerland, Uzbekistan, South Sudan. A very large number of high-quality submissions were received from them 324 were accepted for publication in Procedia Computer Science Journal, Elsevier. All papers have been published in Procedia Computer Science, Elsevier, Vol 235, 2024.

**International Workshop on Artificial Intelligence and Quantum Computing
In association with IBM Research Lab, India**

Special Session Id: **ICMLDE2024.027**

Special Session Title: **Federated Learning for Real Time IoMT-based Edge Computing**

Special Session Chairs **Dr T. Poongodi**

Special Session Co-Chairs: **Dr M Kiruthika, Dr R. Gopal**

IMPORTANT DATES:

Last date for paper submissions: **15th August 2024**

For any additional queries, please email and contact at info@icmlde.org

- Dr. T. Poongodi, Professor, Department of CSE, will serve as the session chair for the Scopus-indexed International Conference, specifically for the special session entitled "Cryptographic Techniques and Security Challenges in the Artificial Intelligence of Things," at the 6th International Conference on Information, Management and Machine Intelligence (ICIMMI-2024), held at Poornima Institute of Engineering & Technology, Jaipur, on December 23-24, 2024.

6TH INTERNATIONAL CONFERENCE ON INFORMATION
MANAGEMENT AND MACHINE INTELLIGENCE (ICIMMI-2024)

CALL FOR PAPERS

**Special Session: Cryptographic techniques and Security
Challenges in Artificial Intelligence of Things**

Scopus Indexed ACM International Conference Proceedings

Submission Deadline **15th September 2024**

TOPICS (NOT LIMITED TO)

- ✓ Cyber-attacks and Defense mechanisms in AIoT
- ✓ Error-prone Communication in IoT Devices
- ✓ Data Management in AIoT Devices
- ✓ Encryption Techniques for Data Security in AIoT
- ✓ Resource Constrained Lightweight Cryptographic Solutions
- ✓ Effective Data Analytics in AIoT devices
- ✓ Threats in AIoT
- ✓ Trustworthy and Interpretable AI for AIoT Security
- ✓ AIoT Network Security
- ✓ AIoT for Industrial Automation
- ✓ Responsible AIoT for Healthcare
- ✓ Developing Smart Society with AIoT
- ✓ AI-Driven Threats and Vulnerabilities with Generative Networks

IMPORTANT DATES

1. 30TH OCTOBER 2024
Acceptance Notification
2. 15TH NOVEMBER 2024
Camera Ready Submission
3. 20TH NOVEMBER 2024
Early bird Registration
4. 25TH NOVEMBER 2024
Late Registration
5. 23,24 DECEMBER 2024
Conference Dates

Info & Submission

<https://icimmi.poornima.org>
[:icimmi@poornima.org](mailto:icimmi@poornima.org)

Organized By:
**Poornima Institute
of Engineering &
Technology**
Sitapura Jaipur,
Rajasthan

SESSION CHAIRS:

Dr. T Poongodi, Professor, Department of Computer Science and Engineering, Dayananda Sagar University, Bangalore, Karnataka, India.

Dr. M Kiruthika, Assistant Professor, Department of Computational Intelligence, SRM Institute of Science and Technology, Kattankulathur, Tamilnadu, India.

- Dr. T. Poongodi, Professor, Department of CSE, will serve as the session chair for the Scopus-indexed International Conference, specifically for the special session entitled "Recent Advancements of Deep Neural Networks in IoMT," at the International Conference on Pervasive Computational Technologies (ICPCT-2025), held at GL Bajaj Institute of Technology and Management, Greater Noida, on February 8-9, 2025.

INTERNATIONAL CONFERENCE
ON
PERVASIVE COMPUTATIONAL TECHNOLOGIES (ICPCT-2025)

CALL FOR PAPERS

Special Session: Recent Advancement of Deep Neural Networks in IoMT

Accepted and presented papers will be submitted for inclusion into IEEE Xplore (Scopus indexed)

Abstract Submission
01 October 2024
Full Paper Submission
31 October 2024

TOPICS (NOT LIMITED TO)

- ✓ Explainable DNN in IoMT edge devices
- ✓ Medical image processing with DNN for IoMT
- ✓ Securing IoMT systems with DNN-based IDS
- ✓ Adaptive ML for medical data
- ✓ Optimization techniques for centralized learning of medical data
- ✓ Ubiquitous health monitoring system based on IoMT & DNN
- ✓ Detection of anomalies using Blockchain in IoMT network
- ✓ Optimization techniques for medical image retrieval in IoMT
- ✓ Privacy preservation of IoMT data with FL
- ✓ Deploying cryptographic methods in edge devices
- ✓ Smart wearables & remote patient monitoring
- ✓ Redefining IoMT based on quantum technologies
- ✓ Medical image retrieval framework with CNN and RNN for IoMT

IMPORTANT DATES

1. 30TH DECEMBER 2024
Acceptance Notification
2. 20TH JANUARY 2025
Registration
3. 25TH JANUARY 2025
Camera Ready Paper
4. 08,09 FEBRUARY 2025
Conference Dates

Info & Submission

<https://www.glbitm.org/icpct-2025/index.html>

Organized By:
GL Bajaj Institute of Technology
Greater Noida, India

SESSION CHAIRS:

Dr. T Poongodi, Professor, Department of Computer Science and Engineering, Dayananda Sagar University, Bangalore, Karnataka, India.

Dr. M Kiruthika, Assistant Professor, Department of Computational Intelligence, SRM Institute of Science and Technology, Kattankulathur, Tamilnadu, India.



Dr. Shreyas Rajendra Hole
Assistant Professor
Department of CSE(AIML)

- Dr. Shreyas Rajendra Hole, Assistant Professor in the Department of CSE (AI&ML), has published a patent under Intellectual Property Rights (IPR) with Application No. 202441063265 A, titled 'SEPIC-Based BESS for Maintaining Maximum Power Point of Solar Panels Using Machine Learning and Method Thereof.

पेटेंट कार्यालय
 शासकीय जर्नल

OFFICIAL JOURNAL
OF
THE PATENT OFFICE

नियमन सं. 35/2024	शुक्रवार	दिनांक 30/08/2024
ISSUE NO. 35/2024	FRIDAY	DATE: 30/08/2024

पेटेंट कार्यालय का एक प्रकाशन
 PUBLICATION OF THE PATENT OFFICE

The Patent Office Journal No. 35/2024 Dated: 30/08/2024 76588

(1) PATENT APPLICATION PUBLICATION (2) Application No. 202441063265 A
 (3) INDIA
 (4) Date of filing of Application: 22/08/2024 (5) Publication Date: 30/08/2024

(6) Title of the invention: SEPIC-BASED BESS FOR MAINTAINING MAXIMUM POWER POINT OF SOLAR PANELS USING ML AND METHOD THEREOF

(1) International Classification: H02M000156000; H02M000700000; G01P000470000; H02M000150000; H02M000120000	(7) Name of Applicant: 1) Drayamada Sagar University, Address of Applicant: Dornakalagaddali, Marolli, Kamalapur Road, Kamalapur District - 562112, Karnataka, India, Marolli
(6) International Application No.: N/A	Name of Applicant: N/A
Filing Date: N/A	Address of Applicant: N/A
(7) International Publication No.: N/A	(7) Name of Invention: 1) Shreyas Rajendra Hole
(8) Prior Art Addition: N/A	Address of Applicant: Drayamada Sagar University Main Campus, Dornakalagaddali, Marolli, Kamalapur Road - 562112, Karnataka, India, N/A
Application Number: N/A	2) Shreyas Rajendra Hole
Filing Date: N/A	Address of Applicant: Drayamada Sagar University Main Campus, Dornakalagaddali, Marolli, Kamalapur Road - 562112, Karnataka, India, N/A
(8) Divisional Application Number: N/A	
Filing Date: N/A	

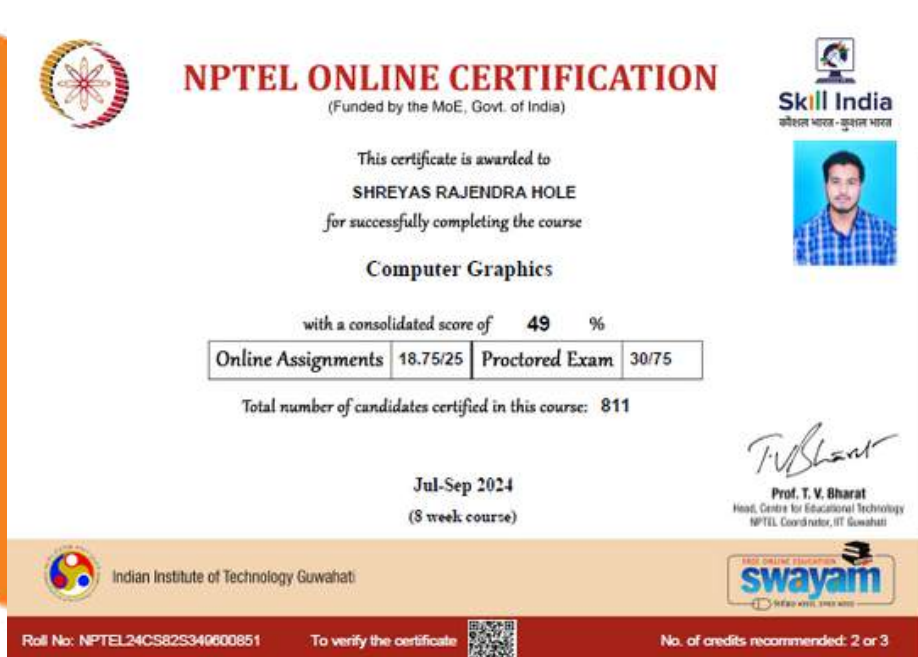
(7) Abstract: A Single-Ended Primary Inductor Converter (SEPIC)-based battery energy storage system (BESS) (100) for maintaining a maximum power point of a solar panel or photovoltaic cell using Machine Learning (ML) model is disclosed. The system (100) includes a feedback circuit (104). The feedback circuit (104) is configured with a Proportional Integral Derivative (PID) controller (110) and the ML model (112) to sense an output voltage from the solar panel or the photovoltaic cell. The sensed voltage is compared with a set point voltage to calculate an error voltage. The ML model (112) generates an error signal based on the calculated error voltage using a Pulse Width Modulation (PWM). The phase control switching operation of the SEPIC is modified to maintain the output voltage constant by varying a width of PWM pulse. --
 No. of Pages: 17 No. of Claims: 9

The Patent Office Journal No. 35/2024 Dated: 30/08/2024 77495

- Dr. Shreyas Rajendra Hole, Assistant Professor in the Department of CSE (AI&ML), received a token of appreciation for his notable service as a reviewer for the 4th International Conference on Intelligent Systems and Machine Learning (4th ICISML-2024), organized by the Department of Artificial Intelligence and Data Science, KPR Institute of Engineering and Technology, Coimbatore, Tamil Nadu, India, from 23rd to 24th August 2024.



- Dr. Shreyas Rajendra Hole, Assistant Professor in the Department of CSE (AI&ML), has been awarded the NPTEL certificate for successfully completing the 8-week (Jul-Sep 2024) NPTEL course on Computer Graphics.



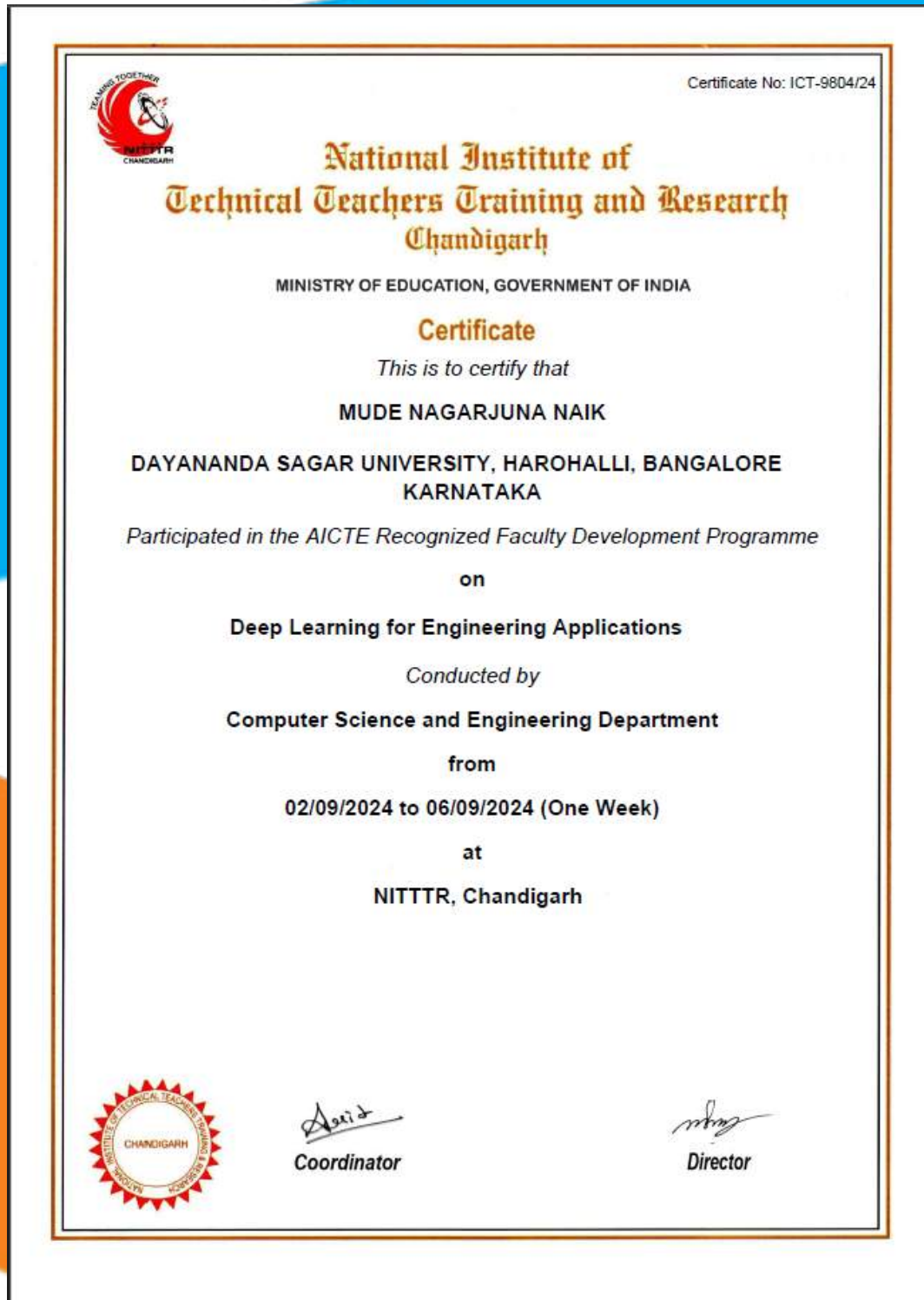


Dr. Mude Nagarjuna Naik
Assistant Professor
Department of CSE(AIML)

- Dr. Mude Nagarjuna Naik, Assistant Professor in the Department of CSE (AI&ML), served as a Reviewer for the 4th International Conference on Computer, Communication, Control and Information Technology (C3IT) 2024, organized by the Academy of Technology, G.T. Road (Adisaptagram), Aedconagar, Hooghly-712121, West Bengal, India.



- Dr. Mude Nagarjuna Naik, Assistant Professor in the Department of CSE (AI&ML), participated in the AICTE-recognized Faculty Development Programme on Deep Learning for Engineering Applications, conducted by the Department of Computer Science and Engineering from 02/09/2024 to 06/09/2024 (one week) at NITTTR, Chandigarh.





Dr. Monika Goyal
Assistant Professor
Department of CSE(AIML)

- Mr. Karthik K. Pai (ENG21AM3013) and Dr. Monika Goyal, Assistant Professor in the Department of CSE (AI&ML), have published a conference paper titled 'Framework for Evaluating the Carbon Footprint of Academic Machine Learning Endeavors' in the 2024 1st International Conference on Communications and Computer Science (InCCCS), Bangalore, India, 2024, pp. 1-7, DOI: 10.1109/InCCCS60947.2024.10593140.



Dr. Vinutha N.
Associate Professor
Department of CSE(AIML)

- Dr. Vinutha N., Associate Professor in the Department of CSE (AI&ML), delivered a talk during a one-day workshop at St. Francis De Sales College on 25/09/2024. The title of the talk was: 'Unraveling the AI Frontier: Deep Learning, Explainable AI and the Emergence of Generative Models.'





Prof. Sriramkumar R.
Assistant Professor
Department of CSE(AIML)

- Prof. Sriramkumar R., Assistant Professor in the Department of CSE (AI&ML), has been awarded the NPTEL certificate for successfully completing the 8-week (Jul-Sep 2024) NPTEL course on Computer Graphics.



- Prof. Sriramkumar R., Assistant Professor in the Department of CSE (AI&ML), successfully presented the paper titled 'Independent Navigation System for Visually Impaired People Using Deep Learning Technique' at the 4th International Conference on Intelligent Systems and Machine Learning (4th ICISML-2024), organized by the Department of Artificial Intelligence and Data Science, KPR Institute of Engineering and Technology, Coimbatore, Tamil Nadu, India, from 23rd to 24th August 2024.





Dr. Dilipkumar Jang Bahadur Saini
Associate Professor
Department of CSE(CY)

- Dr. Dilipkumar Jang Bahadur Saini presented a paper entitled “Innovative Real Estate Management System: Artificial Intelligence-based Segregation” at the 5th International Conference on Smart Electronics and Communication, organized by Kongunadu College of Engineering and Technology, Trichy, Tamil Nadu, India, from 18th to 20th September 2024.



- Dr. Dilipkumar Jang Bahadur Saini presented a paper titled “Novel Approach for Identification of Ayurveda Plants using New-age Technology” at the International Conference on Artificial Intelligence and Networking (ICAIN-2024), jointly organized by Guru Tegh Bahadur Institute of Technology (GTBIT), GGSIPU, Delhi, in association with Portalegre Polytechnic University, Portugal, Institute of Technology and Business in České Budějovice, Czech Republic and Keshav Mahavidyalaya, University of Delhi, on 24th–25th September 2024.





Prof. Sharanabasappa Tadkal
Assistant Professor
Department of CSE(CY)

- Prof. Sharanabasappa Tadkal successfully completed a four-hour workshop on Data Analytics using Power BI, including an end-to-end project, organized by Tech Tip24 on 15th September 2024..



- Prof. Sharanabasappa Tadkal successfully completed the Business Analyst Assessment from Learn Tube by Career Ninja on 29th September 2024.





Prof. Shivamma D
Assistant Professor
Department of CSE(DS)

- Shivamma D has successfully completed the 5-day Faculty Development Program on Building Web Development Solutions with Cloud Computing under the Next Gen Employability Program from 27-08-2024 to 31-08-2024.





Dr. U. Pavan Kumar
Assistant Professor
Department of CSE(DS)

- Dr. U. Pavan Kumar successfully completed the 2-Day Workshop on “Business Intelligence using Power BI” .Organized by Skill Nation on 31/08/2024 and 01/09/2024.





Dr. D. Sudha
Associate Professor
Department of CST

- Dr. D. Sudha, Associate Professor, Department of CST, attended a five-day online faculty development program on “Building Web Development Solutions with Cloud Computing” from 27th August to 31st August 2024, organized by the Next Gen Employability Program.





Prof. Ramandeep Kaur
Assistant Professor
Department of CST

- Prof. Ramandeep Kaur, Assistant Professor published a research article titled “Lexi Genius: An Offline Summarization Brilliance” at the International Conference on Artificial Intelligence and Networking (ICAIn-2024), jointly organized by the Guru Tegh Bahadur Institute of Technology (GTBIT), GGSIPU, Delhi, in association with the Portalegre Polytechnic University, Portugal, the Institute of Technology and Business in České Budějovice, Czech Republic and Keshav Mahavidyalaya, University of Delhi, on 24th–25th September 2024.





Prof. Nivetha NRP
Assistant Professor
Department of CST

- Prof. Nivetha NRP, Assistant Professor, Department of CST attended a five-day online faculty development program on “Building Web Development Solutions with Cloud Computing” from 27th August to 31st August 2024, organized by Next Gen Employability Program.





Prof. Vinayaka V M
Assistant Professor
Department of CST

- Prof. Vinayaka, Assistant Professor, Department of CST attended a five-day online faculty development program on “Building Web Development Solutions with Cloud Computing” from 27th August to 31st August 2024, organized by Next Gen Employability Program.





**Prof. Yashaswini BV,
Assistant Professor
Department of CST**

- Prof. Yashaswini BV, Assistant Professor, Department of CST attended a five-day online faculty development program on “Building Web Development Solutions with Cloud Computing” from 27th August to 31st August 2024, organized by Next Gen Employability Program.





Dr. G K Suryanarayana
Professor
Department of ASE

- Dr. Suryanarayana G.K., Professor and Mr. Mohammed Amin from the Department of Aerospace Engineering have published a patent on "Nano-Air Vehicle for Low-Altitude, Low-Speed Flying" on September 20, 2024.



Prof. Sripad Kulkarni
Assistant Professor
Department of ASE

- Prof. Sripad Kulkarni, Assistant Professor from the Department of Aerospace Engineering was awarded a certificate of recognition for guiding and mentoring students in the successful completion of the Phase-I Engineering Design Internship Program (Y 2023-24) for the coursework on converting a 'Drone Winch Mechanism' to a 3D Digital Twin, from Dassault Systems La Fondation.





Prof. Divyashree H B
Assistant Professor
Department of ECE

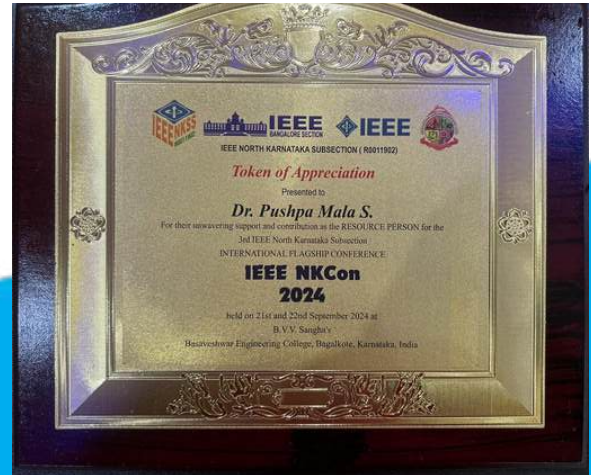
- Divyashree H B, Assistant Professor, Department of Electronics & Communication Engineering completed her Ph.D. under the guidance of Dr. Puttamadappa C, Registrar, Dayananda Sagar University and Professor in the Department of Electronics & Communication Engineering, DSU, SoE. Co-Guide: Dr. Nandini Prasad K S, Dean Foreign Affairs & HOD, Department of Information Science Engineering, DSATM, Bangalore.





Dr. Pushpa Mala S
Associate Professor
Department of ECE

- Dr. Pushpa Mala S, Associate Professor was invited as a Resource Person for IEEE NKCon 2024, the flagship International Conference of the IEEE North Karnataka Subsection, held in association with the IEEE Bangalore Section in Bagalkot from 21st to 22nd September 2024.
- Adithya H. K., Aishwarya Kulkarni, Archana B., Chandana K. T. and Dr. Pushpa Mala S. presented a paper titled 'Exploring Memristor Integrated R-2R DAC: A Study in Circuit Optimization and Performance' at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.
- Adithya H. K., Aishwarya Kulkarni, Archana B., Chandana K. T. and Dr. Pushpa Mala S. presented a paper titled 'A Study on Circuit Optimization and Performance of Memristor-Integrated 3-bit Pipeline DAC' at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.
- Ramesh N E and Dr. Pushpa Mala S. presented a paper titled 'A novel method to determine bedrock from a single sensor-based seismic reflection signal using HAAR wavelet' at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.



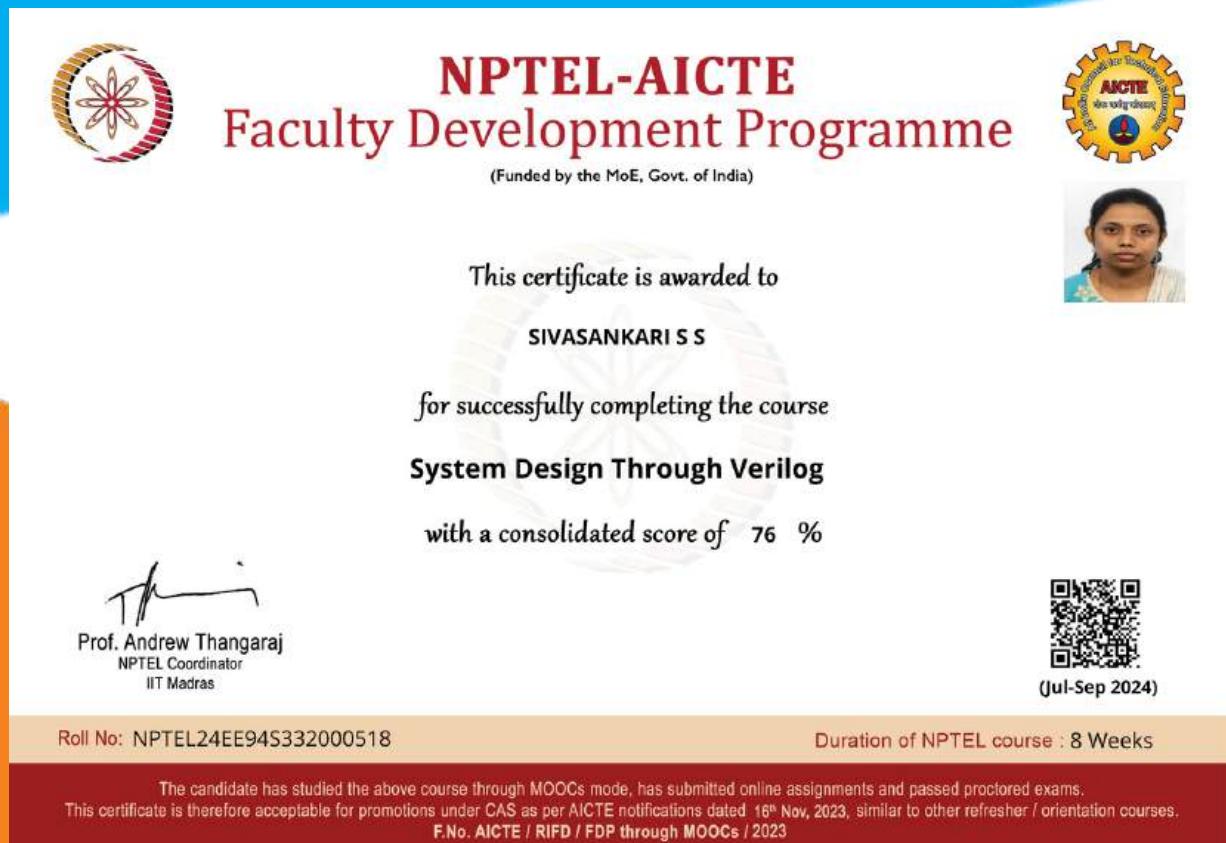
- Dr. Pushpa Mala S, Associate Professor served as a reviewer for the International Conference on Vehicular Technology and Transportation Systems (ICVTTS) 2024. The conference, held on the 27th and 28th of September in Bangalore, India, was organized by the IEEE Vehicular Technology Society (VTS), Bangalore Section.





Prof. Sivasankari S. S.
Assistant Professor
Department of ECE

- Ms. Sivasankari S. S., Assistant Professor in the Department of Electronics and Communication Engineering, successfully completed the NPTEL-AICTE Faculty Development Programme on 'System Design Through Verilog' with a consolidated score of 76%.

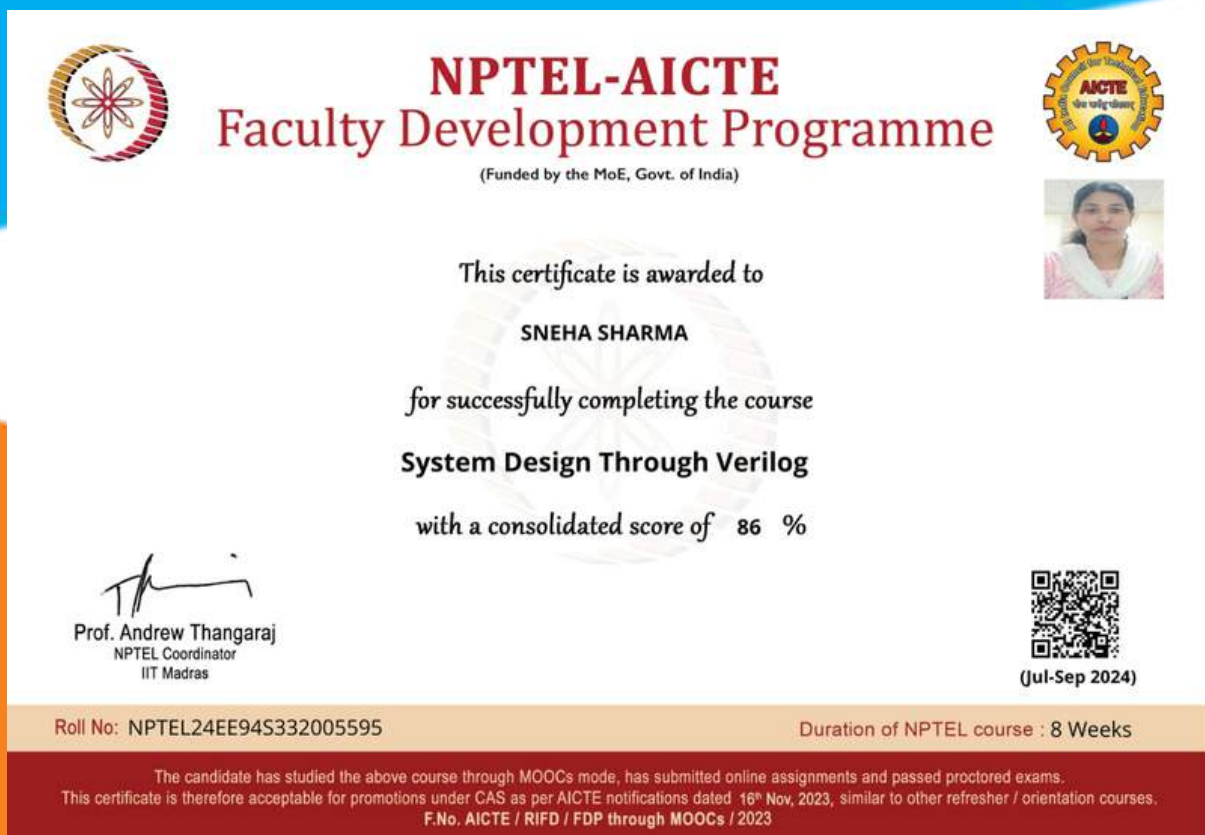


The certificate features the NPTEL logo on the left and the AICTE logo on the right. The central text reads: 'NPTEL-AICTE Faculty Development Programme (Funded by the MoE, Govt. of India)'. Below this, it states 'This certificate is awarded to SIVASANKARI S S for successfully completing the course System Design Through Verilog with a consolidated score of 76 %'. A signature of Prof. Andrew Thangaraj, NPTEL Coordinator at IIT Madras, is on the left. A QR code and the date '(Jul-Sep 2024)' are on the right. At the bottom, the roll number 'NPTEL24EE94S332000518' and 'Duration of NPTEL course : 8 Weeks' are listed. A footer note states: 'The candidate has studied the above course through MOOCs mode, has submitted online assignments and passed proctored exams. This certificate is therefore acceptable for promotions under CAS as per AICTE notifications dated 16th Nov, 2023, similar to other refresher / orientation courses. F.No. AICTE / RIFD / FDP through MOOCs / 2023'.



Dr. Sneha Sharma
Assistant Professor
Department of ECE

- Dr. Sneha Sharma, Assistant Professor in the Department of Electronics and Communication Engineering, successfully completed the NPTEL-AICTE Faculty Development Programme on "System Design Through Verilog" with a consolidated score of 86%.



The certificate features the NPTEL logo on the left and the AICTE logo on the right. The central text reads: "NPTEL-AICTE Faculty Development Programme (Funded by the MoE, Govt. of India). This certificate is awarded to SNEHA SHARMA for successfully completing the course System Design Through Verilog with a consolidated score of 86 %". It includes a signature of Prof. Andrew Thangaraj, NPTEL Coordinator at IIT Madras, and a QR code. The bottom section contains the roll number NPTEL24EE94S332005595, a duration of 8 weeks, and a disclaimer: "The candidate has studied the above course through MOOCs mode, has submitted online assignments and passed proctored exams. This certificate is therefore acceptable for promotions under CAS as per AICTE notifications dated 16th Nov, 2023, similar to other refresher / orientation courses. F.No. AICTE / RIFD / FDP through MOOCs / 2023".



Dr. Venkataramanan N S
Associate Professor
Department of Chemistry

- Dr. N.S. Venkataramanan, Associate Professor has been listed among the top 2% of the most influential scientists in the world, as published by Stanford University, USA, in August 2024. He has been recognized for his work in Chemical Physics, particularly in understanding noncovalent interactions and drug delivery.



Dr. Rakesh Kumar Singh
Assistant Professor
Department of Mathematics

- Dr. Rakesh Kumar Singh, Assistant Professor has published an article in the IOP Conference Series: Earth and Environmental Science (Vol. 1382, No. 1, p. 012010), IOP Publishing (SCOPUS), on September 10, 2024, entitled "Study of Pollutant Transport Under Linear Sorption in a Groundwater Reservoir."

1st Global Summit on Sustainable Science and Technology (GS3T) IOP Publishing
IOP Conf. Series: Earth and Environmental Science 1382 (2024) 012010 doi:10.1088/1755-1315/1382/1/012010

Study of pollutant transport under linear sorption in a groundwater reservoir

Tapan Paul¹, Nav Kumar Mahato^{1,*}, Rakesh Kumar Singh² and Pintu Das³

¹Department of Mathematics, School of Basic and Applied Sciences, Adamas University Kolkata-700126, India

²Department of Mathematics, School of Engineering, Dayananda Sagar University, Bengaluru-562112, India

³Department of Mathematics, Sarala Birla University, Ranchi, Jharkhand-835103, India

* Corresponding author. E-mail: nkm.adamasuniversity@gmail.com

Abstract. Pollutant dispersion in a porous medium can be modelled mathematically to forecast its concentration distribution profile. The governing equation of pollutant transport is modelled as a partial differential equation. Effects of sorption, zero-order production rate and first-order decay rate on pollutant transport are incorporated in the present study. The temporarily dependent pulse-type input source decaying exponentially is assumed at the origin of the domain. The solution of the proposed transport equation is derived by Laplace transform method for the respective time split domain. Matlab algorithm is developed to illustrate the obtained analytical solutions graphically for various hydrological input data. The pollutant distribution profiles in the medium are obtained for different times and porosities. The pollutant transport nature is examined for various forms of velocity flow pattern in presence and absence of input source. Also, the derived solution is compared graphically under special case with the existing solution and found the results with good agreement.

Keywords: Analytical solution, Dispersion, Groundwater reservoir, Pollutant transport, Porous medium, Sorption.



SCHOOL OF ENGINEERING



STUDENT ACHIEVEMENTS

- Shashi Kumar C has successfully achieved 1st place in Engineering the Future. Ideathon competition organized by the club YANTROVE from the department of CSE(AI&ML) ON 18th September 2024.



- Mr. Chandan N. S. (ENG22CS0038), a 5th-semester CSE student, has successfully completed a LinkedIn Learning course on “What is Generative AI?” in August 2024. He also completed internships as a Python Programming Intern from August 12, 2024, to September 8, 2024, at Internpe and as a Web Development Intern from August 1, 2024, to September 1, 2024, at Coding Raja Technologies.





- Mr. Chandan N. S. (ENG22CS0038), a 5th-semester CSE student, has successfully completed 100% of the self-paced training courses on MATLAB Onramp on September 10, 2024 and Image Processing Onramp on September 13, 2024, through MathWorks Training Services.



- Final-year students Amshu (ENG21AS0008) and Sharon (ENG21AS0001) from the Department of Aerospace Engineering presented their concept on a fluid-structure scenario titled "Development and Optimization of a Hydraulic Thrust Reverser Prototype for Enhanced Aircraft Deceleration" for the FPSI Yoganarashimha Award, under the guidance of Prof. Sripad, Assistant Professor, Department of Aerospace Engineering, DSU.



- Ms. Kiran Jeevan Waghmare (ENG18PPCS04), Research Scholar in the Department of Computer Science and Engineering, completed her Ph.D. defence VIVA-VOCE entitled "Optimization of Multivariate Pattern Analysis Using Brain-Computer Interface" under the guidance of Dr. Meenakshi Malhotra on September 4, 2024, in Lecture Hall 2, A Block, SOE, DSU, Harohalli, Bangalore.



- Mrs. Harshitha A. N. (ENG22CS0068), a 5th-semester student in the Department of CSE at Dayananda Sagar University's School of Engineering, has successfully completed an 8-week online training program on Data Science. The training consisted of modules on Introduction to Data Science, Data Analysis Fundamentals, Introduction to Data Visualization, Working with Data, Predictive Analytics Using Machine Learning, AI in Data Science and a Capstone Project, completed on September 5, 2024, through Internshala Trainings.



- Mr. Saurabh Kumar (ENG23CS0446), a 3rd-semester CSE student, has successfully completed a Skill Development Workshop from Skill Intern on August 9, 2024.



- Mr. Hrushikesh Raghavendra (ENG22CS0321), a 5th-semester CSE student, has successfully completed the ISRO online course on AI/ML for Geodata Analysis, held from August 19 to August 23, 2024.



- Mr. Yash Soni (ENG22CS0211), a 5th-semester CSE student, participated in the website-making competition "Web Wizard," based on a Halloween theme, organized by IEEE ComSoc and the IEEE Student Branch. He was awarded first prize on September 13, 2024.



- Ms. Manavi P. (ENG23CS0101), a 3rd-semester CSE student, participated in the website-making competition “Web Wizard,” based on a Halloween theme, organized by IEEE ComSoc and the IEEE Student Branch. She was awarded the third prize on September 13, 2024.



- Mr. Sahil Singh (ENG23CS0435), Mr. Aadarsh Kumar (ENG23CS0238) and Mr. Arji Jethin Naga Sai Eswar (ENG23CS0267), 3rd-semester CSE students, successfully participated in the website-making competition “Web Wizard,” based on a Halloween theme, organized by IEEE ComSoc and the IEEE Student Branch at DSU on September 13, 2024.



- Mr. Omkar S. G. (ENG23CS0128), a 3rd-semester CSE student, participated in the “Engineer the Future: Ideathon” competition organized by the YANTROVE club from the Department of CSE (Artificial Intelligence and Machine Learning) on September 18, 2024 and won 1st place in the competition.



- Ms. Krishna Rajesh (ENG22CS0556), a 5th-semester CSE student, participated in the “Engineer the Future: Ideathon” competition organized by the YANTROVE club from the Department of CSE (Artificial Intelligence and Machine Learning) on September 18, 2024. She also performed a dance at the Onam celebration on September 20, 2024, at DSU Harohalli.




- The team HORIZON GUARD, consisting of Mr. Darshan Anand (ENG21AM0028, 7th sem - AIML), Mr. Anuj Dwivedi (ENG21AM0011, 7th sem - AIML), Mr. Srihari K.B. (7th sem - Cybersecurity) and Mr. Naindeep Singh (7th sem - CSE), secured 2nd place in the VTION Digital Hackathon, presented by Hack Culture, after an intense 45-day challenge with 250 participants. The team proudly took home a cash prize of ₹75,000.



- Mr. Anuj Dwivedi (ENG21AM0011), a 7th-semester AI & ML student, was honored as the Chief Guest for the Science Club Inauguration at Ryan International School on August 28, 2024.



- Mr. Darshan Anand (ENG21AM0028), a final-year student in the AI & ML department, has been appointed as a NASA Space Apps Navigator at the NASA International Space Apps Challenge. In this prestigious role, he will serve as a global panelist for universal event judging and mentor participating teams. This renowned event, which last year attracted 57,999 participants from 152 countries, fosters global innovation and collaboration, bringing together some of the brightest minds from around the world.

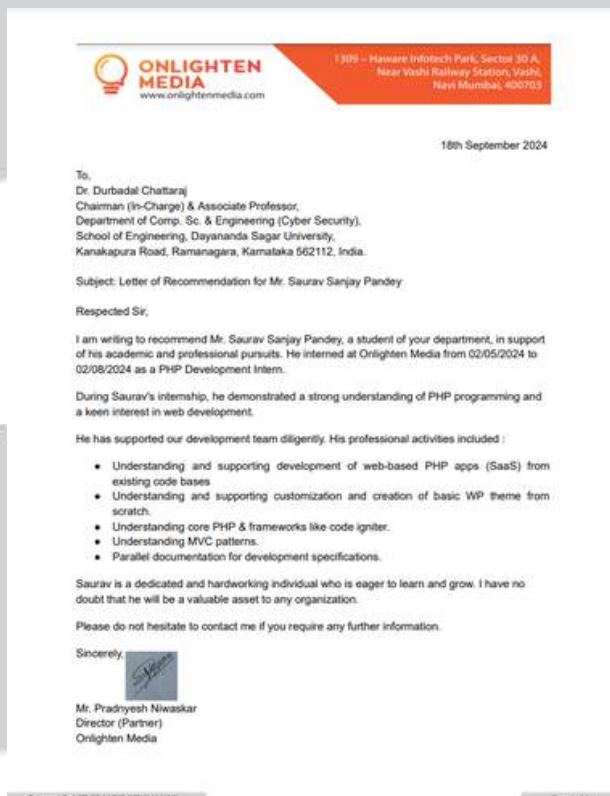


Darshan Anand
NASA Space Apps Navigator

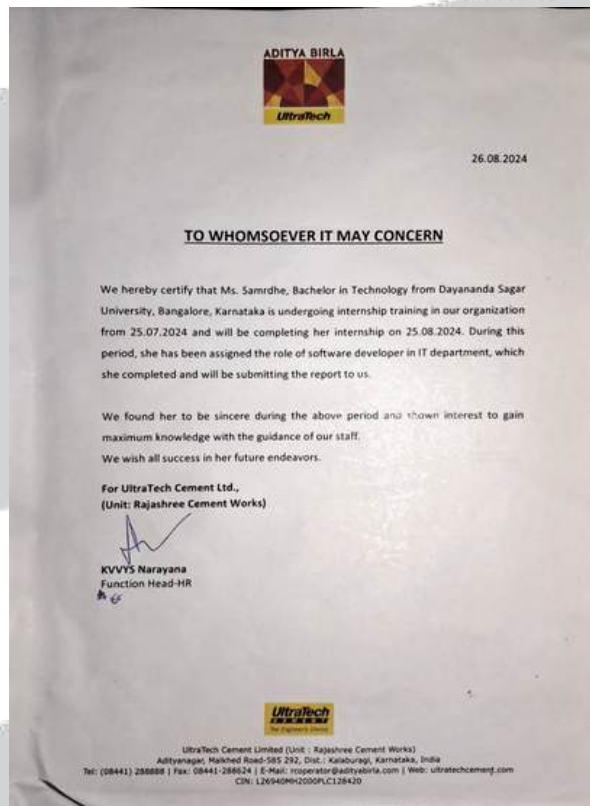
- Mr. Srihari K B (ENG21CY0043) from Team Horizon Guard participated and was adjudged as a runner-up in the “VTION Innovate Hackathon,” organized by Vidhi Techinnovation Opportunities Network Private Limited in partnership with FHS Accelerator Private Limited and Hack and Grow Consultancy Private Limited on 19th September 2024.



- Mr. Saurav Sanjay Pandey (ENG22CY0039) successfully completed his internship at Onlighten Media from 2nd May 2024 to 2nd August 2024. During the internship, he demonstrated a strong understanding of PHP programming and showed a keen interest in web development.



- Ms. Samrdhe (ENG22CY0022) was assigned the role of Software Developer in the IT department during her internship training from 25th July 2024 to 25th August 2024 at Rajashree Cement Works, Malkhed.



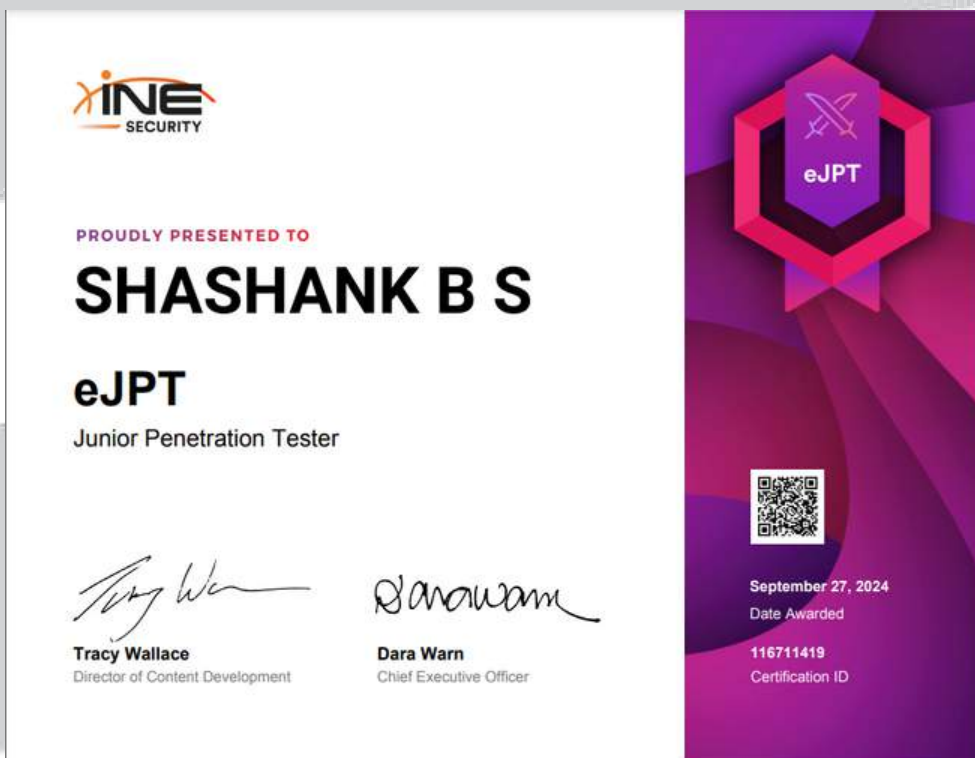
- Mr. Afwan (ENG23CY0094) successfully completed his offline internship at Modex Trading Pvt. Ltd. from 13th August 2024 to 13th September 2024. During his internship, he was exposed to various activities in security operations.



- Ms. Bindhurani KP (ENG21CY0010) attended and successfully completed the course "Introduction to the Dark Web, Anonymity and Cryptocurrency" by EC-Council on 24th February 2024, with certificate number 299522.



- Mr. Shashank B S (ENG21CY0040) has been certified as a "Junior Penetration Tester" by eJPT, a hands-on, entry-level Red Team certification that simulates skills used during real-world engagements, on 27th September 2024.



- Ms. Dhivya B and Ms. Lavanya S from the 5th semester CST won first prize in the NATIONAL LEVEL SYMPOSIUM “INNOVISM,” conducted by Project Contest Innovation LLP, India.



- Ms. B. Dhivya (ENG22CT0004) has successfully completed the GUVI course “Generative AI” on September 22, 2024.



- Ms. Lavanya R (ENG21CT0018) published a research article titled “Lexi Genius: An Offline Summarization Brilliance” at the International Conference on Artificial Intelligence and Networking (ICAIn-2024), jointly organized by the Guru Tegh Bahadur Institute of Technology (GTBIT), GGSIPU, Delhi, in association with the Portalegre Polytechnic University, Portugal, the Institute of Technology and Business in České Budějovice, Czech Republic and Keshav Mahavidyalaya, University of Delhi, on 24th–25th September 2024.



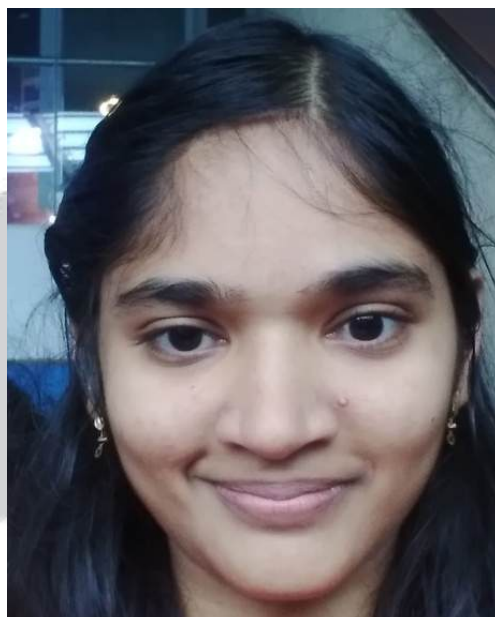
- Ms. Sowmya L G (ENG21CT0017) published a research article titled “Lexi Genius: An Offline Summarization Brilliance” at the International Conference on Artificial Intelligence and Networking (ICAIn-2024), jointly organized by the Guru Tegh Bahadur Institute of Technology (GTBIT), GGSIPU, Delhi, in association with the Portalegre Polytechnic University, Portugal, the Institute of Technology and Business in České Budějovice, Czech Republic and Keshav Mahavidyalaya, University of Delhi, on 24th–25th September 2024



- Ms. Harshitha R (ENG21CT0008) published a research article titled “Lexi Genius: An Offline Summarization Brilliance” at the International Conference on Artificial Intelligence and Networking (ICAIn-2024), jointly organized by the Guru Tegh Bahadur Institute of Technology (GTBIT), GGSIPU, Delhi, in association with the Portalegre Polytechnic University, Portugal, the Institute of Technology and Business in České Budějovice, Czech Republic and Keshav Mahavidyalaya, University of Delhi, on 24th–25th September 2024.



- Ms. Vidushi Modi (ENG21CT0048) of 7th semester CST was selected in the KPMG Campus Placement Drive organized by the Department of Training and Placement on 27th September 2024.



- Ms. Swathi S (ENG21CT0043) has successfully completed the Infosys Springboard course “AWS Certified Cloud Practitioner - 2022” on September 20,



- Ms. Manasvini V (ENG22CT0013) has successfully completed 4 weeks of virtual Internship in Data Science with wonderful remarks at Codsoft from 20/07/2024 to 20/08/2024.



- Ms. Pranati Biswal (ENG22CT0016) has successfully completed 4 weeks of virtual Internship in Web Development with wonderful remarks at Codsoft from 10/07/2024 to 10/08/2024.



- Ms. D Jayanth (ENG22CT0027) has successfully completed 4 weeks of virtual Internship in Web Development with wonderful remarks at Codsoft from 05/08/2024 to 05/09/2024.



- The Federation of Karnataka Chambers of Commerce and Industry, Bengaluru, conducted the Manthan-2024 event, which was a business plan competition. Students from the 7th semester participated and received participation certificates.



- Ms. B. Dhivya (ENG22CT0004) has actively participated in the awareness workshop and cleared the required assessment as a part of the Pepshe Supply Chain Start Program, hosted and conducted by PepsiCo India & Internshala.



- Adithya H. K.(ENG20EC0003), Aishwarya Kulkarni(ENG20EC0005), Archana B.(ENG20EC0013), Chandana K. T.(ENG20EC0023) and Dr. Pushpa Mala S. presented a paper titled "Exploring Memristor Integrated R-2R DAC: A Study in Circuit Optimization and Performance" at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.
- Adithya H. K.(ENG20EC0003), Aishwarya Kulkarni(ENG20EC0005), Archana B.(ENG20EC0013), Chandana K. T.(ENG20EC0023) and Dr. Pushpa Mala S. presented a paper titled "A Study on Circuit Optimization and Performance of Memristor-Integrated 3-bit Pipeline DAC" at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.
- Ramesh N E(ENG17PPEC04) and Dr. Pushpa Mala S. presented a paper titled "A novel method to determine bedrock from a single sensor-based seismic reflection signal using HAAR wavelet" at the International Conference of the IEEE North Karnataka subsection, held in association with the IEEE Bangalore Section at Bagalkot from the 21st to the 22nd of September 2024.



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